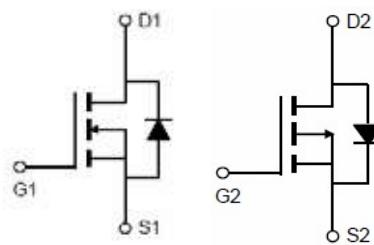


# AP3003

## N and P-Channel Power MOSFET

### Description

The AP3003 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge. This device is suitable for use as a Battery protection or in other Switching application.



### General Features

- N-Channel

- $V_{DS} = 30V, I_D = 4.2A$

$R_{DS(ON)} < 24m\Omega$  @  $V_{GS}=10V$

$R_{DS(ON)} < 28m\Omega$  @  $V_{GS}=4.5V$

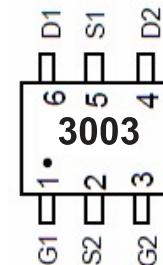
- P-Channel

$V_{DS} = -30V, I_D = -3.7A$

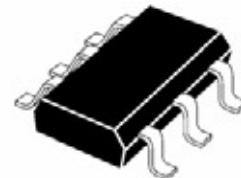
$R_{DS(ON)} < 65 m\Omega$  @  $V_{GS}=-10V$

$R_{DS(ON)} < 85 m\Omega$  @  $V_{GS}=-4.5V$

N-channel      P-channel  
Schematic diagram



Marking and pin Assignment



TSOT23-6L top view

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
3003	AP3003S6	TSOT23-6L	Ø180mm	8mm	3000units

### Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	$V_{DS}$	30	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	$\pm 12$	V
Continuous Drain Current $T_A=25^\circ C$	$I_D$	4.2	-3.7	A
		3	-2.1	
Pulsed Drain Current <sup>(Note 1)</sup>	$I_{DM}$	20	-15	A
Maximum Power Dissipation	$P_D$	1.2		
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	-55 To 150	°C

### Thermal Characteristic

Thermal Resistance, Junction-to-Ambient <sup>(Note2)</sup>	$R_{\theta JA}$	N-Ch	104	°C/W
Thermal Resistance, Junction-to-Ambient <sup>(Note2)</sup>	$R_{\theta JA}$	P-Ch	104	°C/W

## AP3003

## Nand P-Channel Power MOSFET

N-CH Electrical Characteristics ( $T_A=25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	33	-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	0.7	-	1.3	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=4.2\text{A}$	-	19	24	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=2\text{A}$	-	24	28	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=5\text{V}, I_{\text{D}}=3.1\text{A}$	-	4	-	S
<b>Dynamic Characteristics</b> (Note 4)						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	210	-	PF
Output Capacitance	$C_{\text{oss}}$		-	35	-	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	23	-	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=15\text{V}, R_{\text{L}}=3\Omega$ $V_{\text{GS}}=10\text{V}, R_{\text{GEN}}=6\Omega$	-	4.5	-	nS
Turn-on Rise Time	$t_{\text{r}}$		-	1.5	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	18.5	-	nS
Turn-Off Fall Time	$t_{\text{f}}$		-	15.5	-	nS
Total Gate Charge	$Q_{\text{g}}$	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=3.5\text{A}, V_{\text{GS}}=10\text{V}$	-	5	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	0.55	-	nC
Gate-Drain Charge	$Q_{\text{gd}}$		-	1	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=3.5\text{A}$	-	0.8	1.2	V
Diode Forward Current (Note 2)	$I_{\text{s}}$		-	-	3.5	A

## Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

## AP3003

## Nand P-Channel Power MOSFET

**P-CH Electrical Characteristics ( $T_A=25^\circ C$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-30	-33	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-30V, V_{GS}=0V$	-	-	-1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.7	-	-1.3	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-3.7A$	-	50	65	$m\Omega$
		$V_{GS}=-4.5V, I_D=-2A$		60	85	$m\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=-10V, I_D=-2.7A$		2	-	S
<b>Dynamic Characteristics</b> (Note 4)						
Input Capacitance	$C_{iss}$	$V_{DS}=-15V, V_{GS}=0V,$ $F=1.0MHz$	-	199	-	PF
Output Capacitance	$C_{oss}$		-	47	-	PF
Reverse Transfer Capacitance	$C_{rss}$		-	28	-	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-15V, R_L=15\Omega$ $V_{GS}=-10V, R_{GEN}=6\Omega$	-	8	-	nS
Turn-on Rise Time	$t_r$		-	5	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	12	-	nS
Turn-Off Fall Time	$t_f$		-	4	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=-15V, I_D=-2.7A, V_{GS}=-10V$	-	5	-	nC
Gate-Source Charge	$Q_{gs}$		-	0.7	-	nC
Gate-Drain Charge	$Q_{gd}$		-	1.1	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	$V_{SD}$	$V_{GS}=0V, I_s=-2.7A$	-	-	-1.2	V

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

# AP3003

## N and P-Channel Power MOSFET

### N- Channel Typical Electrical and Thermal Characteristics

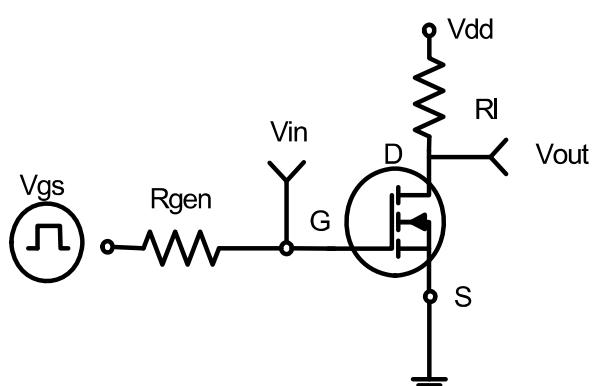


Figure 1:Switching Test Circuit

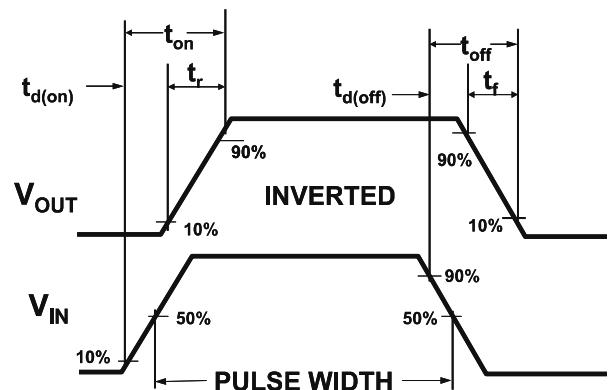


Figure 2:Switching Waveforms

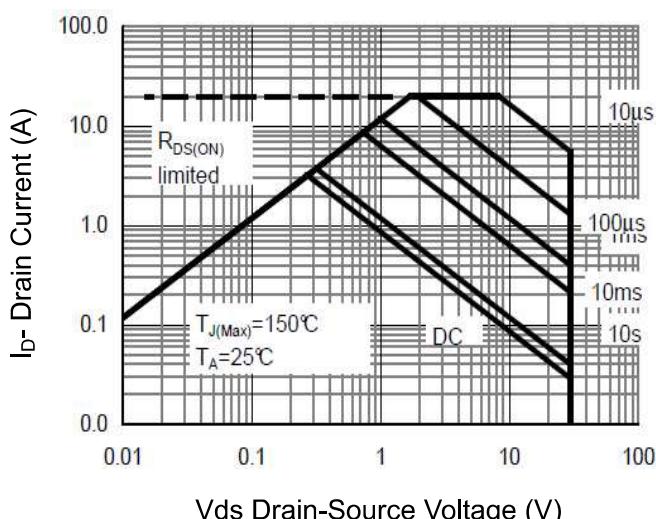
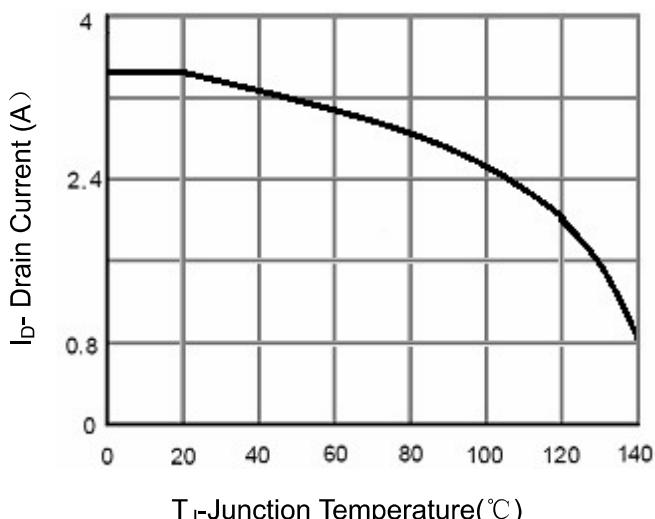


Figure 3 Safe Operation Area



T<sub>J</sub>-Junction Temperature(°C)

Figure 4 Drain Current

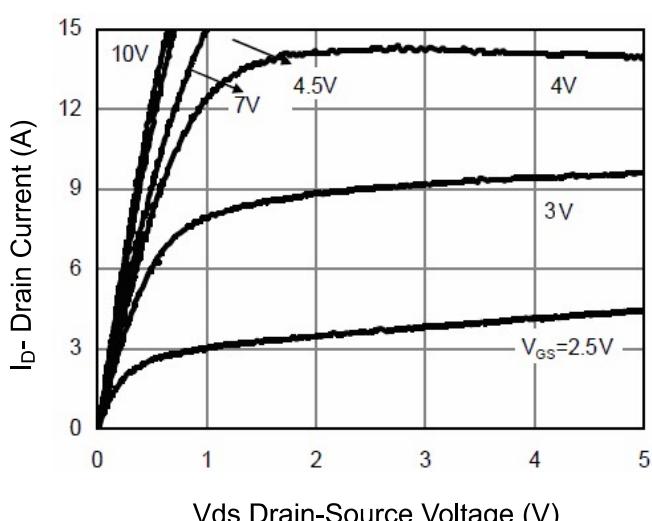


Figure 5 Output Characteristics

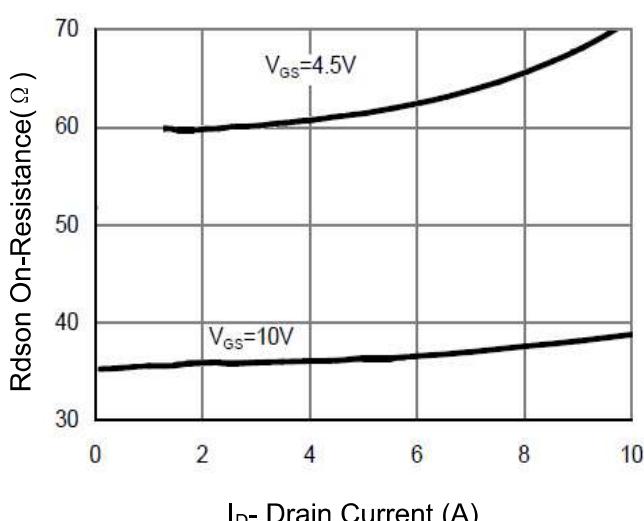
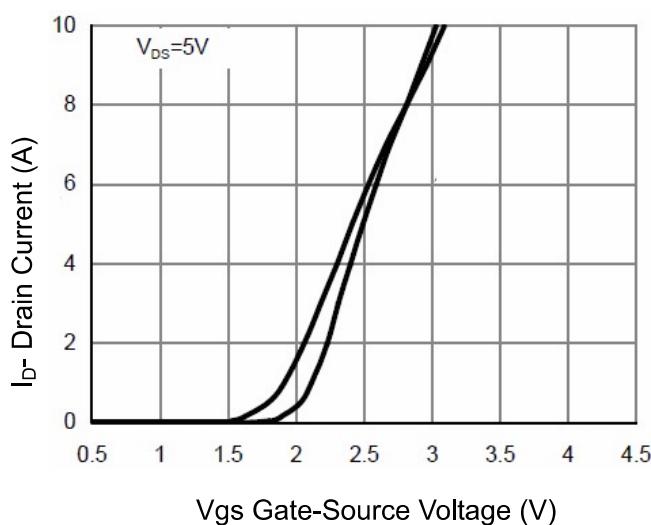


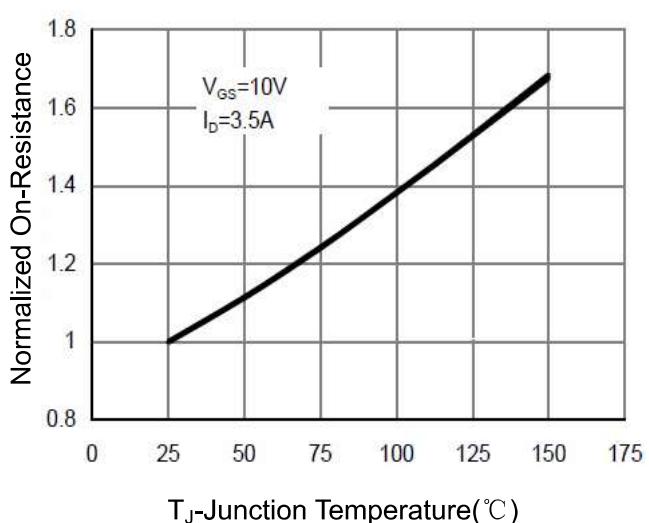
Figure 6 Drain-Source On-Resistance

# AP3003

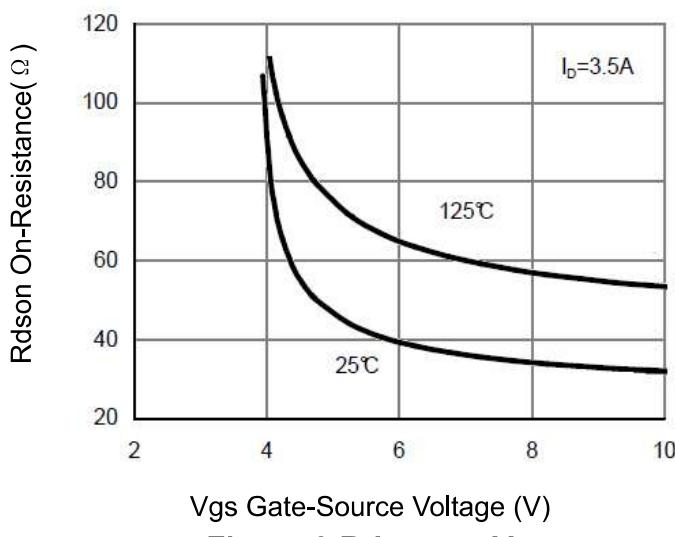
## N and P-Channel Power MOSFET



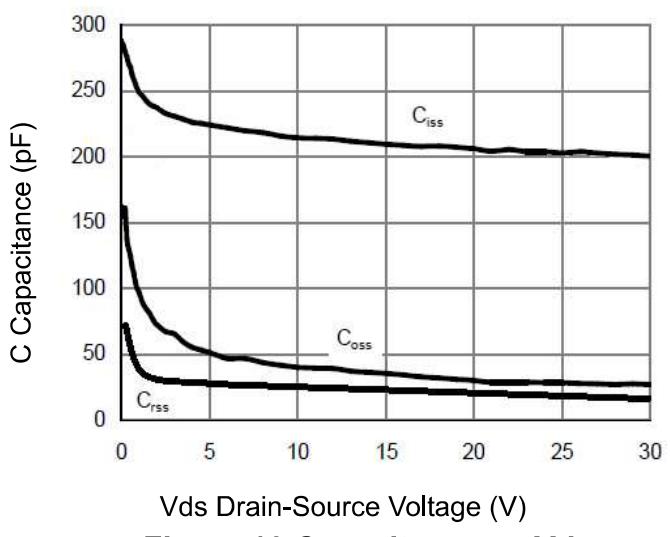
**Figure 7 Transfer Characteristics**



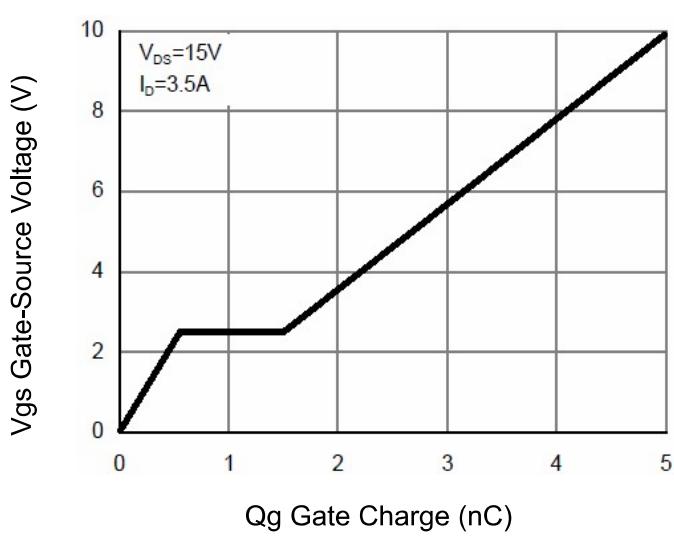
**Figure 8 Drain-Source On-Resistance**



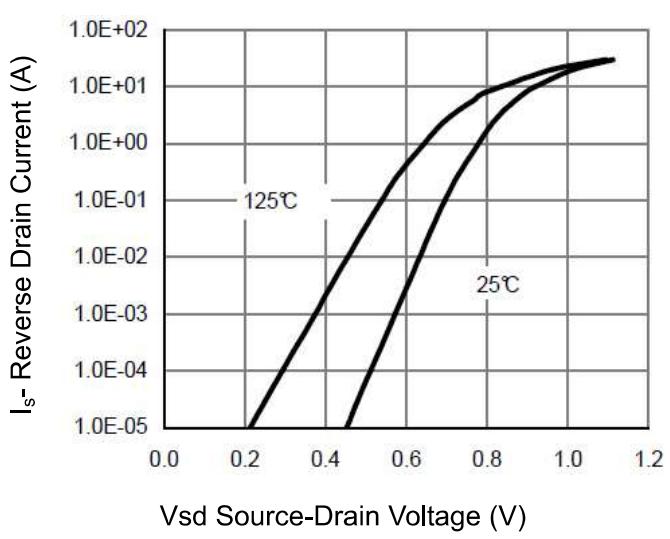
**Figure 9  $R_{DSON}$  vs  $V_{GS}$**



**Figure 10 Capacitance vs  $V_{DS}$**



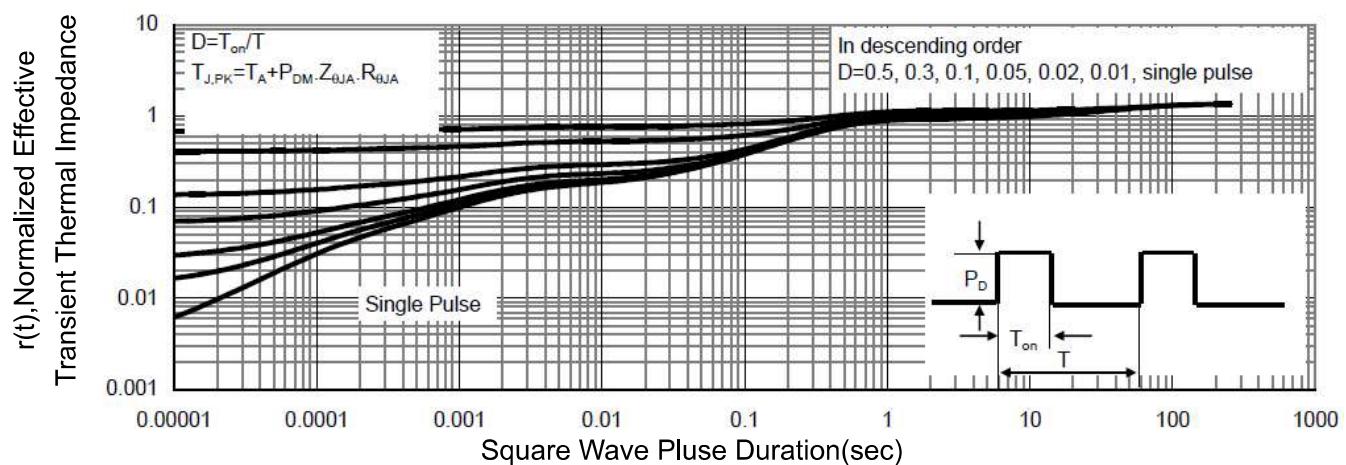
**Figure 11 Gate Charge**



**Figure 12 Source-Drain Diode Forward**

# AP3003

## N and P-Channel Power MOSFET



**Figure 13 Normalized Maximum Transient Thermal Impedance**

# AP3003

## N and P-Channel Power MOSFET

### P- Channel Typical Electrical and Thermal Characteristics

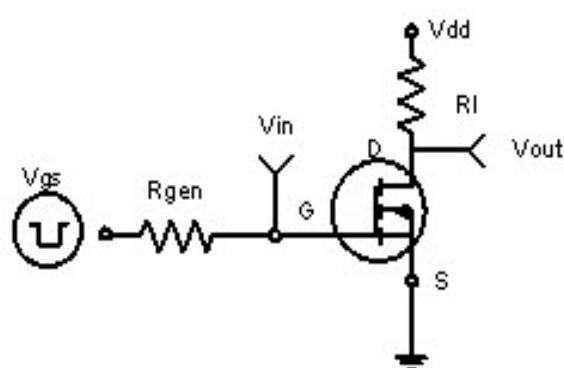


Figure 1:Switching Test Circuit

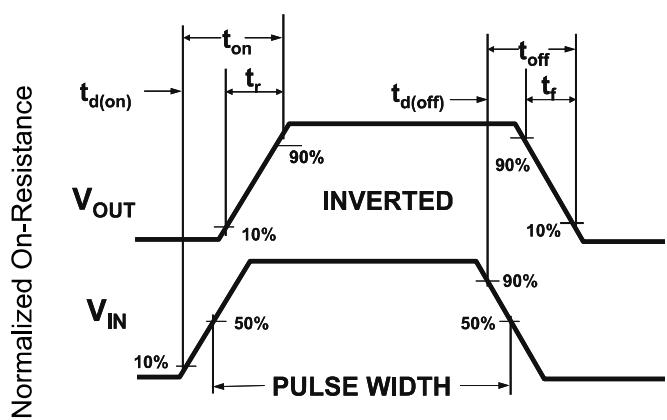


Figure 2:Switching Waveforms

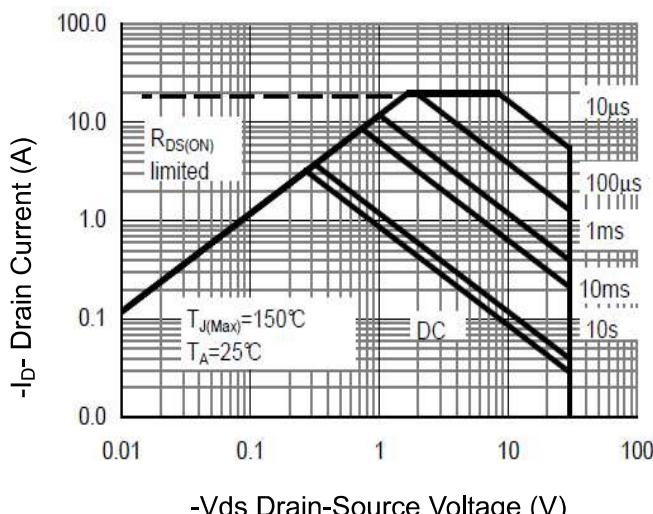


Figure 3 Safe Operation Area

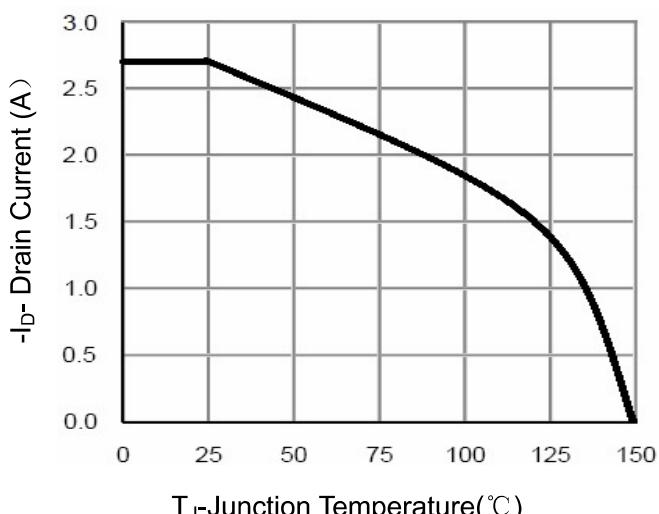


Figure 4 Drain Current

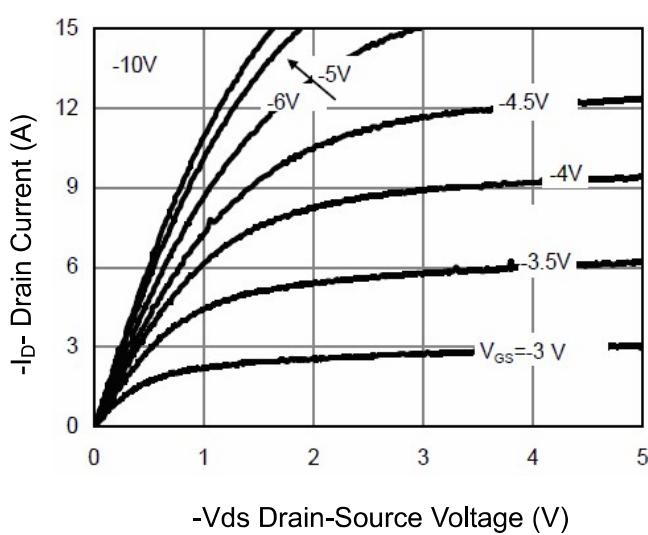


Figure 5 Output Characteristics

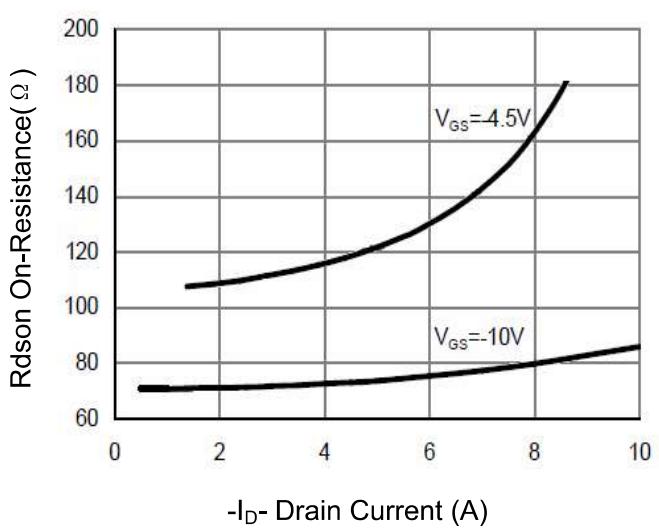


Figure 6 Drain-Source On-Resistance

# AP3003

## N and P-Channel Power MOSFET

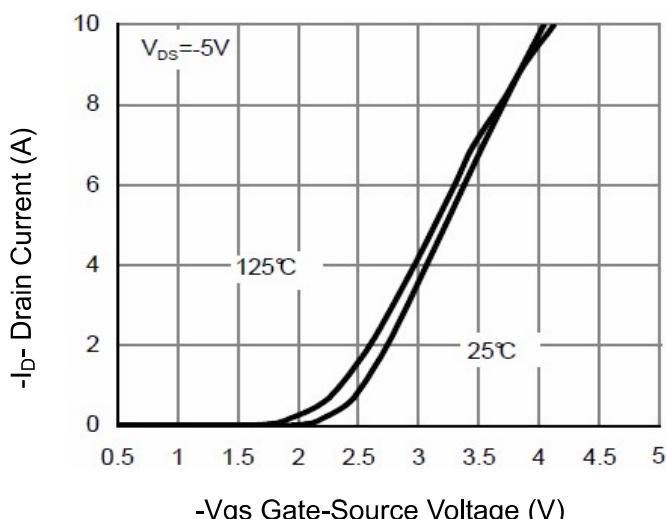


Figure 7 Transfer Characteristics

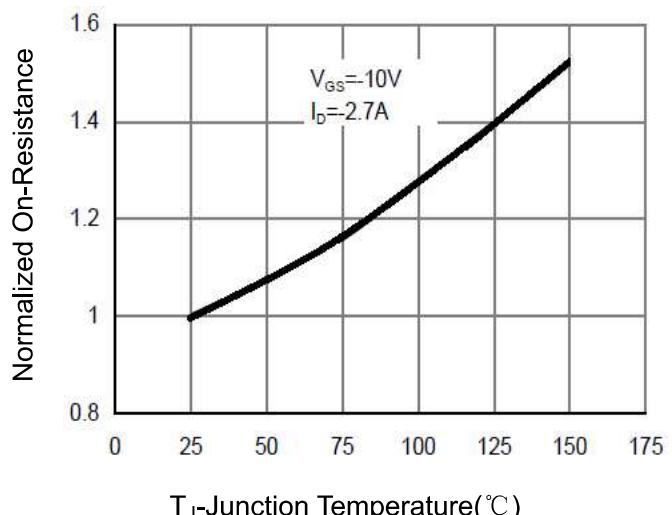


Figure 8 Drain-Source On-Resistance

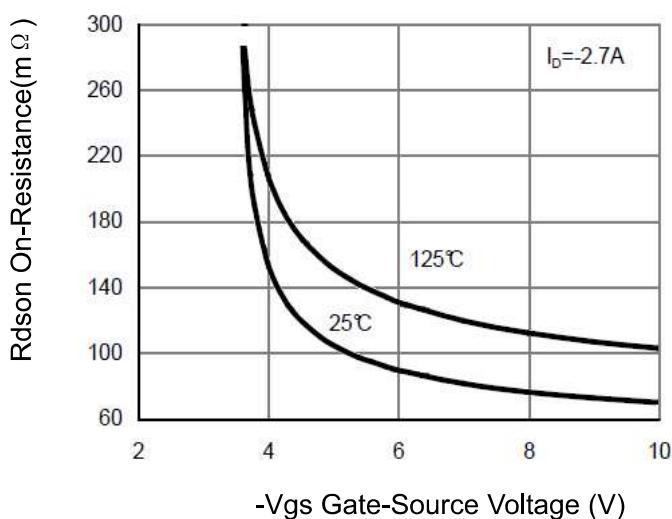


Figure 9  $R_{DS(on)}$  vs  $V_{GS}$

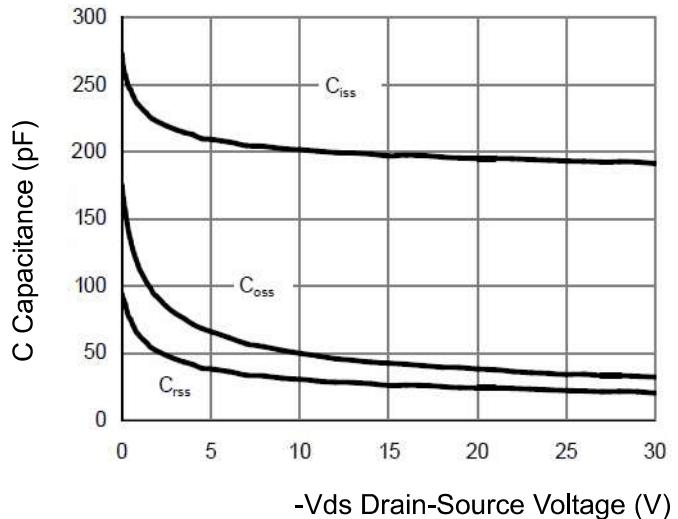


Figure 10 Capacitance vs  $V_{DS}$

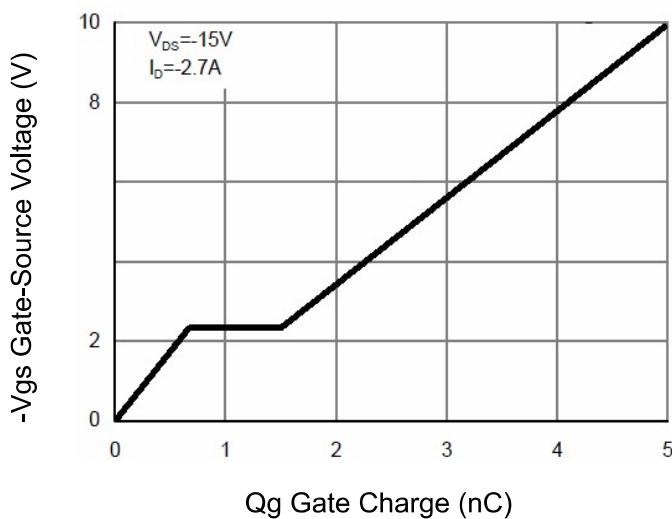


Figure 11 Gate Charge

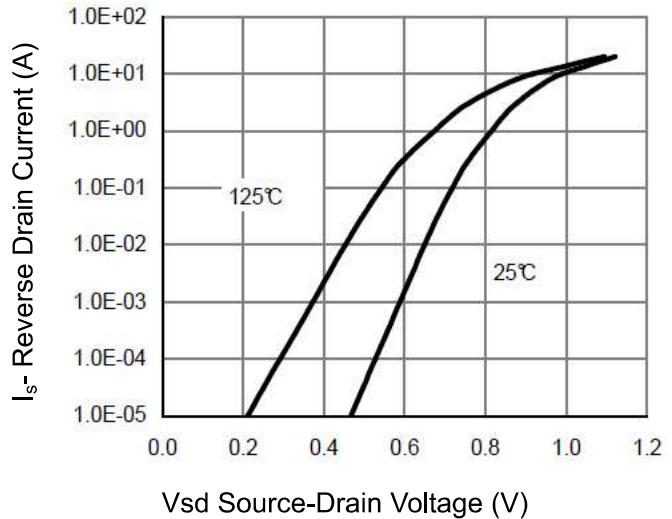
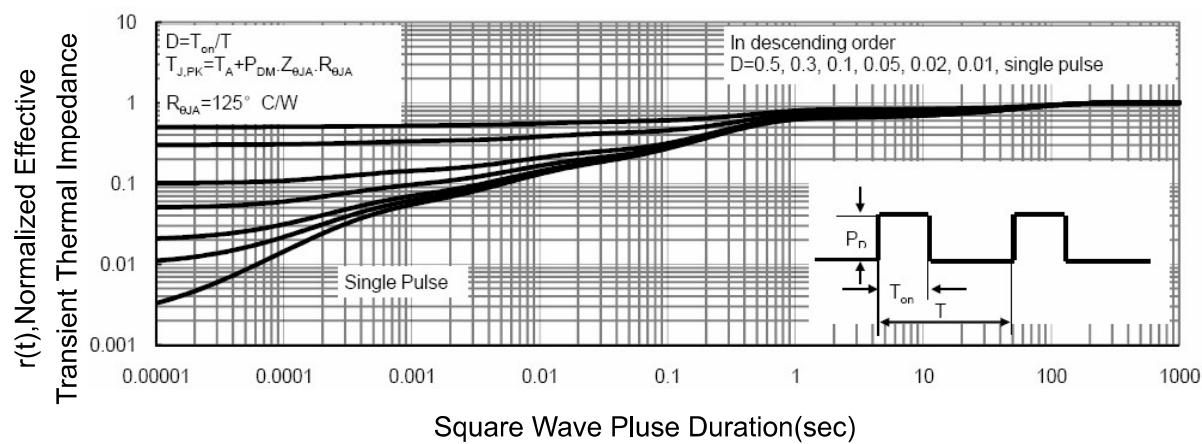


Figure 12 Source-Drain Diode Forward

# AP3003

## N and P-Channel Power MOSFET

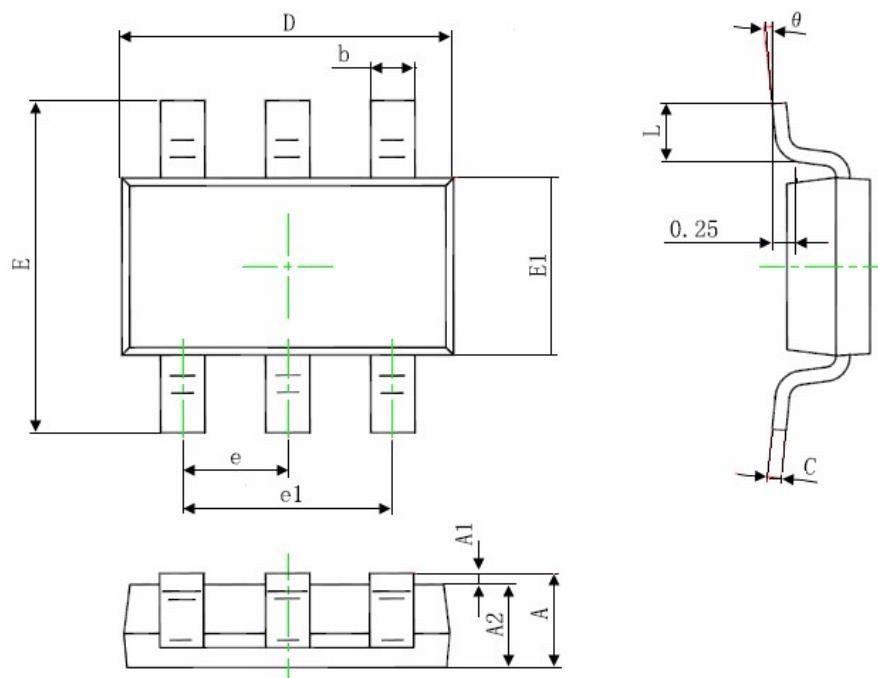


**Figure 13 Normalized Maximum Transient Thermal Impedance**

# AP3003

## N and P-Channel Power MOSFET

### TSOT23-6L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	---	0.900	---	0.035
A1	0.000	0.100	0.000	0.004
A2	0.700	0.800	0.028	0.031
b	0.350	0.500	0.014	0.020
c	0.080	0.200	0.003	0.008
D	2.820	3.020	0.111	0.119
E1	1.600	1.700	0.063	0.067
E	2.650	2.950	0.104	0.116
e	0.95 (BSC)		0.037(BSC)	
e1	1.90 (BSC)		0.075(BSC)	
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°