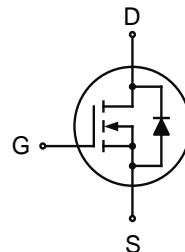


Features

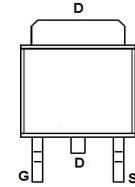
- 650V,7A
- $R_{DS(on)} < 640\text{m}\Omega$ @ $V_{GS}=10\text{V}$ TYP:523m Ω
- advanced super junction technology
- extremely low on resistance



Schematic Diagram

Applications

- Power factor correction (PFC)
- Switched mode power supplies (SMPS)
- Uninterruptible power supply (UPS)
- LED lighting power



TO-252

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
A65R640KM	APA65R640KM	TO-252	-	-	2500

ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	650	V
Gate-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current ($T_c=25^\circ\text{C}$)	I_D	7	A
Continuous Drain Current ($T_c=100^\circ\text{C}$)	I_D	4.4	A
Pulsed Drain Current ⁽¹⁾	I_{DM}	28	A
Single Pulsed Avalanche Energy ⁽²⁾	E_{AS}	249	mJ
Drain Power Dissipation	P_D	48	W
Thermal Resistance from Junction to Case	$R_{\theta JC}$	4.17	$^\circ\text{C}/\text{W}$
Thermal Resistance- Junction to Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55~+150	$^\circ\text{C}$
Maximum Lead temperature for soldering Purpose	T_L	300	$^\circ\text{C}$

Typical Performance Characteristics

Figure 1.On-Region Characteristics

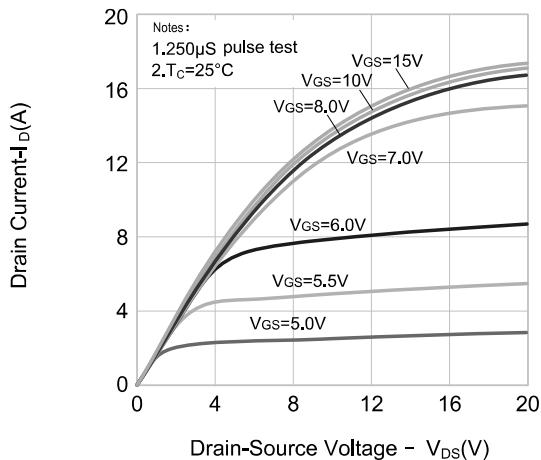


Figure 2.Transfer Characteristics

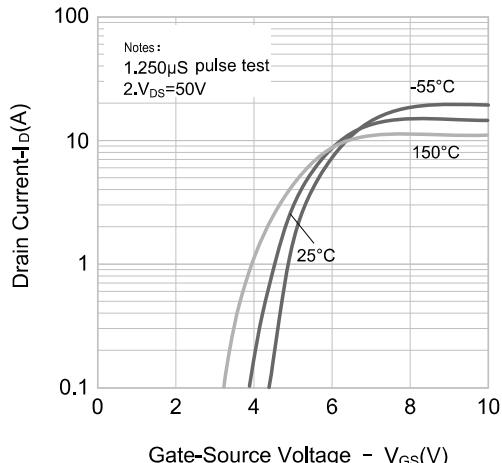


Figure 3.On-Resistance Variation vs. Drain-Current, Gate Voltage

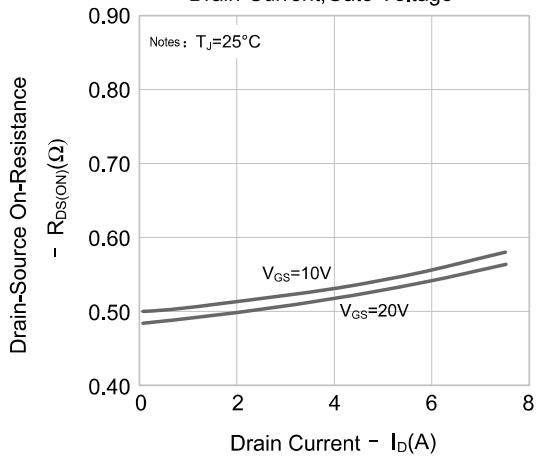


Figure 4.Body Diode Forward Voltage Variation vs.Source Current and Temperature

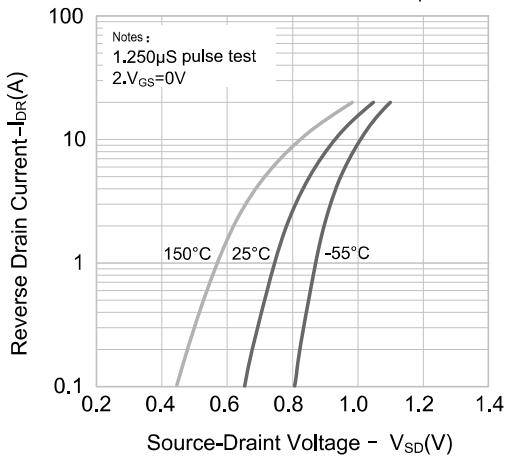


Figure 5.Capacitance Characteristics

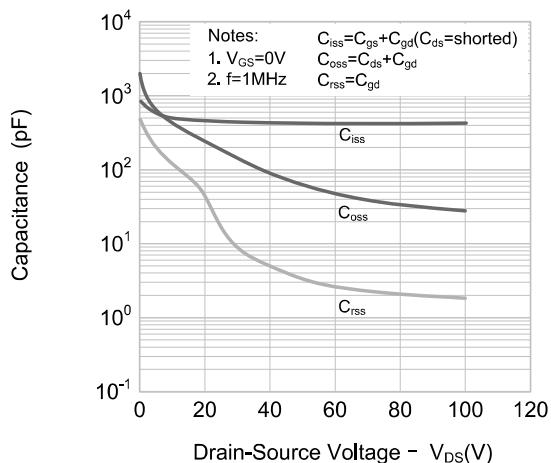
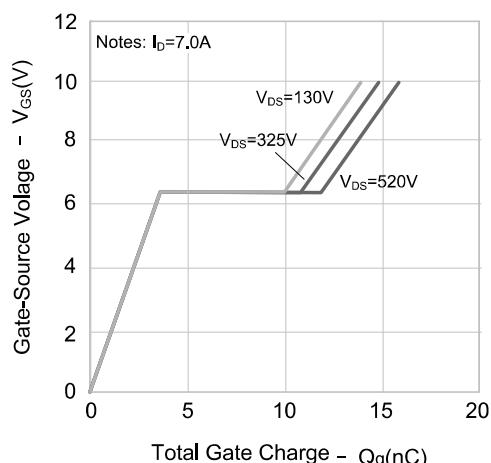


Figure 6.Gate Charge Characteristics



Typical Performance Characteristics

Figure 7. Breakdown Voltage Variation vs. Temperature

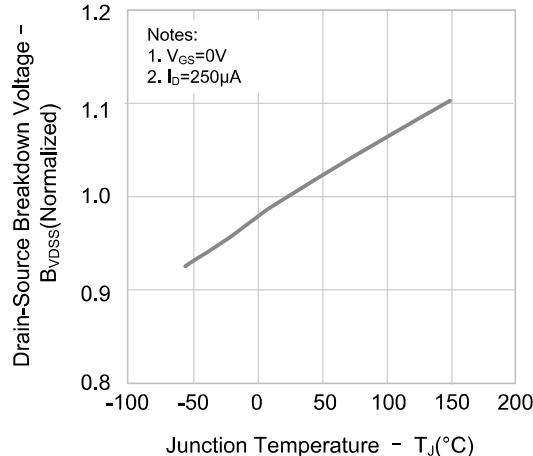


Figure 8. On-resistance Variation vs. Temperature

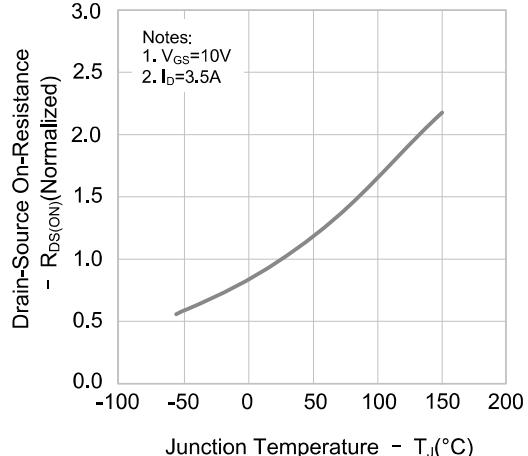


Figure 9.1 Max. Safe Operating Area (SCD65R640C)

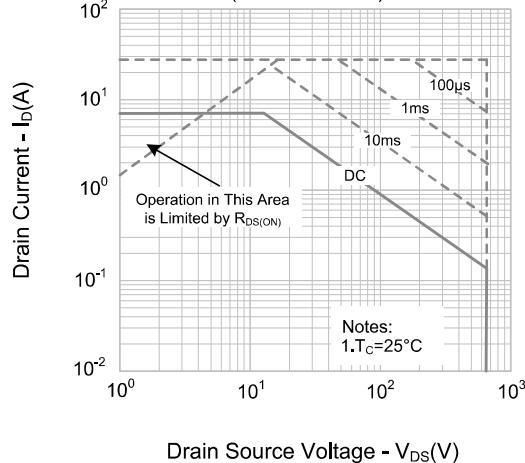
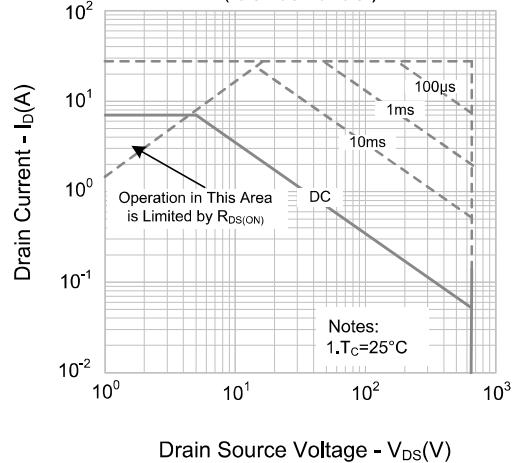
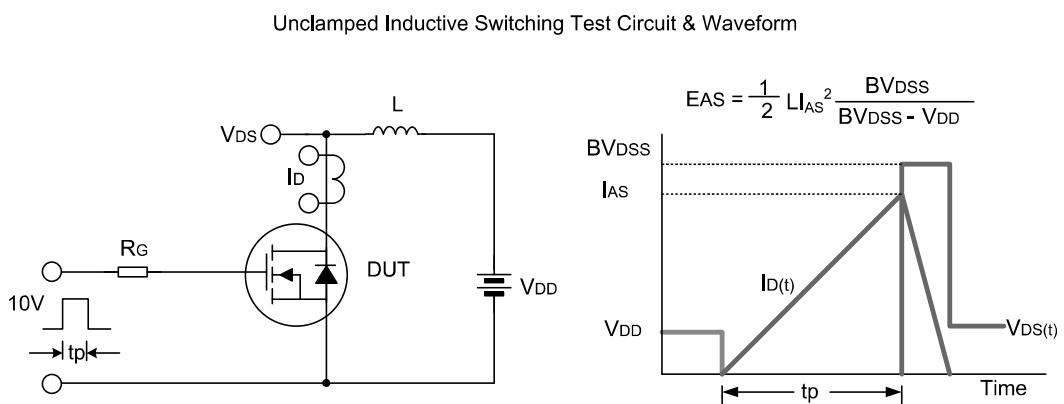
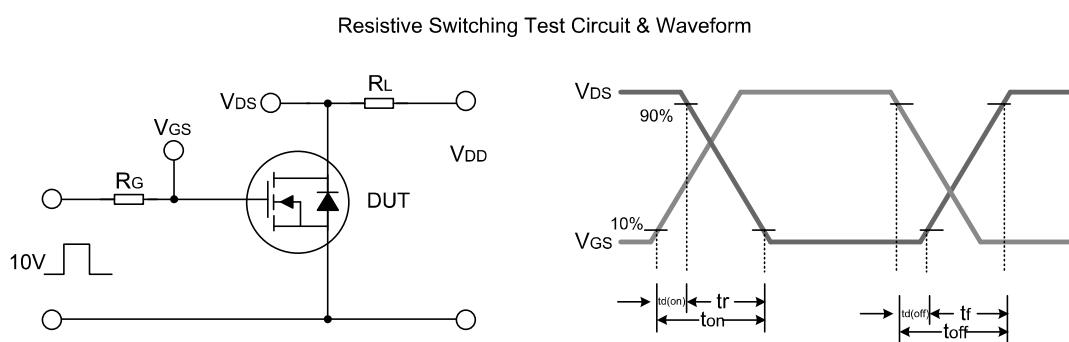
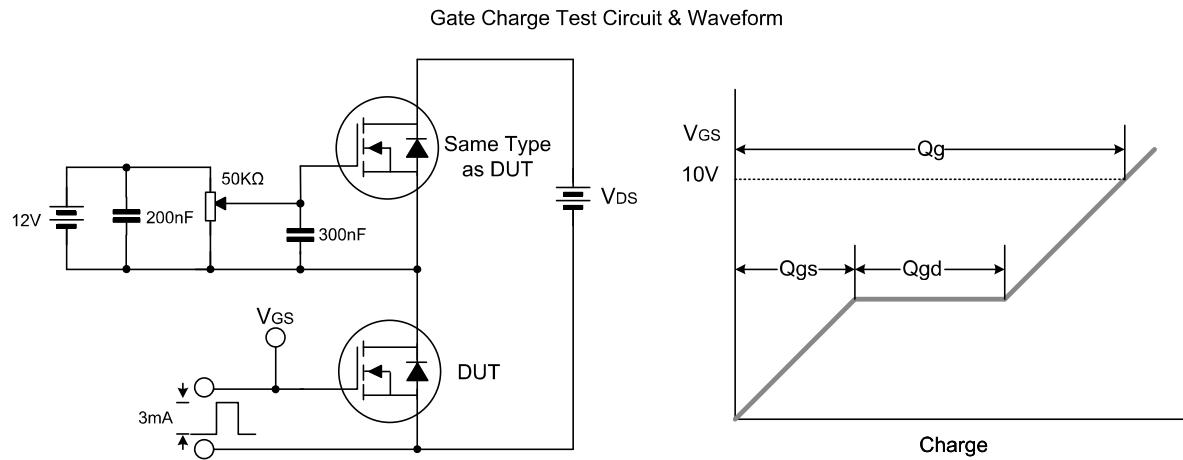


Figure 9.2 Max. Safe Operating Area (SCF65R640C)



Test Circuit



Package Dimensions of TO-252

