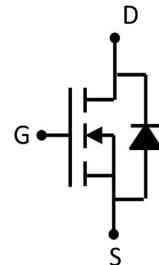


## Feature

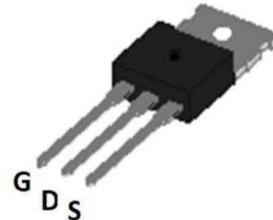
- 200V,9A
- $R_{DS(on)} < 300\text{m}\Omega @ V_{GS}=10\text{V}$  TYP:200 mΩ
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



Schematic Diagram

## Application

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



TO-220

## Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
9N20	AP9N20L	TO-220	-	-	1000

## ABSOLUTE MAXIMUM RATINGS ( $T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	200	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	9	A
Pulsed Drain Current <sup>(1)</sup>	$I_{DM}$	36	A
Single Pulsed Avalanche Energy <sup>(2)</sup>	$E_{AS}$	125	mJ
Avalanche Current <sup>(1)</sup>	$I_{AS}$	5	A
Repetitive Avalanche Energy <sup>(1)</sup>	$E_{AR}$	69	mJ
Power Dissipation ( $T_c = 25^\circ\text{C}$ )	$P_D$	74	W
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.7	°C/W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	60	°C/W
Junction Temperature	$T_J$	150	°C
Storage Temperature	$T_{STG}$	-55~+150	°C

**MOSFET ELECTRICAL CHARACTERISTICS( $T_J=25^\circ\text{C}$  unless otherwise noted)**

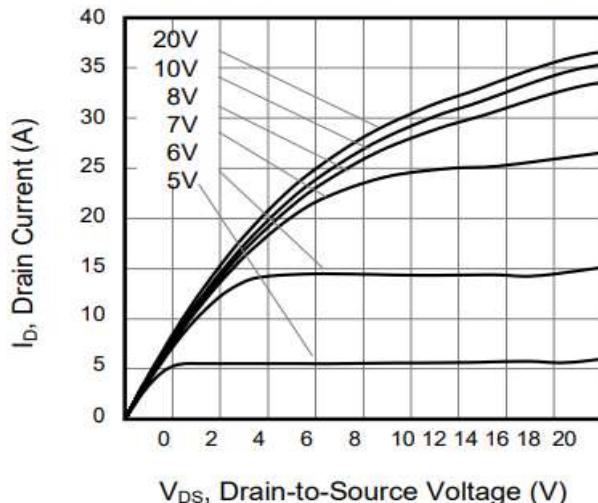
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Static Characteristics</b>						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	200	-	-	V
Zero gate voltage drain current	$I_{\text{DSS}}$	$V_{\text{DS}} = 200\text{V}, V_{\text{GS}} = 0\text{V}$	-	-	1	$\mu\text{A}$
Gate-body leakage current	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$	-	-	$\pm 100$	nA
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	1.5	2.5	V
Drain-source on-resistance <sup>(3)</sup>	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 4.5\text{A}$	-	200	300	$\text{m}\Omega$
<b>Dynamic characteristics</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 25\text{V}, V_{\text{GS}} = 0\text{V}, f = 100\text{kHz}$	-	605	-	pF
Output Capacitance	$C_{\text{oss}}$		-	87	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	37	-	
<b>Switching characteristics</b>						
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 100\text{V}, I_D = 9\text{A}, R_G = 25\Omega$	-	35	-	ns
Turn-on rise time	$t_r$		-	7	-	
Turn-off delay time	$t_{\text{d}(\text{off})}$		-	98	-	
Turn-off fall time	$t_f$		-	32	-	
Total Gate Charge	$Q_g$	$V_{\text{DS}} = 160\text{V}, I_D = 9\text{A}, V_{\text{GS}} = 10\text{V}$	-	19	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	3	-	
Gate-Drain Charge	$Q_{\text{gd}}$		-	8	-	
<b>Source-Drain Diode characteristics</b>						
Diode Forward voltage <sup>(3)</sup>	$V_{\text{DS}}$	$V_{\text{GS}} = 0\text{V}, I_S = 4.5\text{A}, V_{\text{GS}} = 10\text{V}$	-	-	1.4	V
Diode Forward current <sup>(4)</sup>	$I_S$	$T_c = 25^\circ\text{C}$	-	-	9	A
Body Diode Reverse Recovery Time	$t_{\text{rr}}$	$T_J = 25^\circ\text{C}, IF = 9\text{A}, di/dt = 100\text{A/us}$		145		ns
Body Diode Reverse Recovery Charge	$Q_{\text{rr}}$	$T_J = 25^\circ\text{C}, IF = 9\text{A}, di/dt = 100\text{A/us}$		0.82		uc

**Notes:**

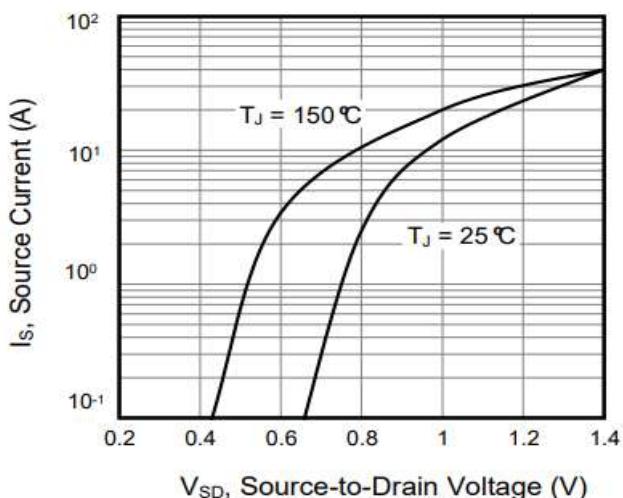
1. Repetitive Rating: pulse width limited by maximum junction temperature
2. L=10mH, VDD = 50V, RG = 25 Ω, Starting TJ = 25 °C
3. Pulse Test: pulse width≤300μs, duty cycle≤1%

**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

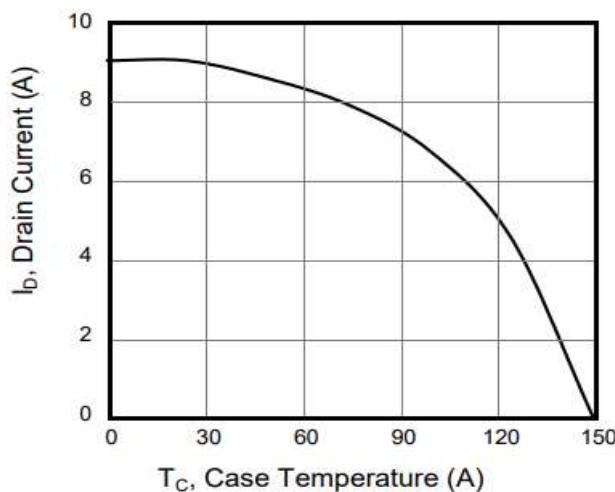
**Figure 1. Output Characteristics ( $T_J = 25^\circ\text{C}$ )**



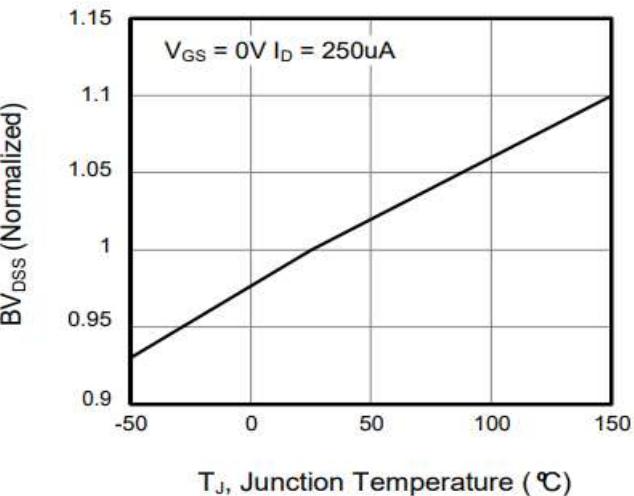
**Figure 2. Body Diode Forward Voltage**



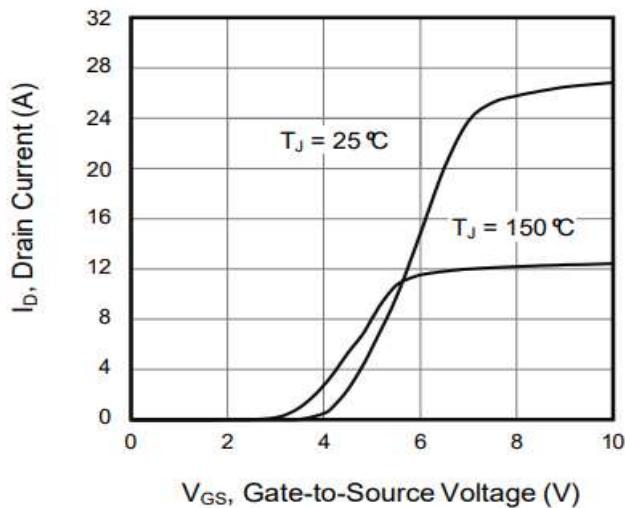
**Figure 3. Drain Current vs. Temperature**



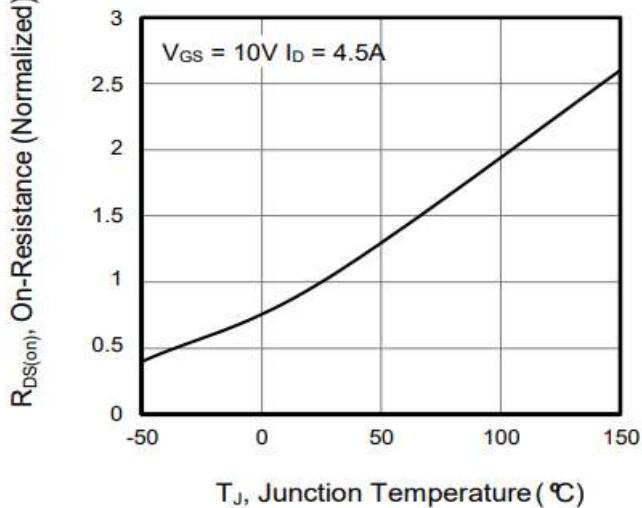
**Figure 4.  $\text{BV}_{DSS}$  Variation vs. Temperature**



**Figure 5. Transfer Characteristics**

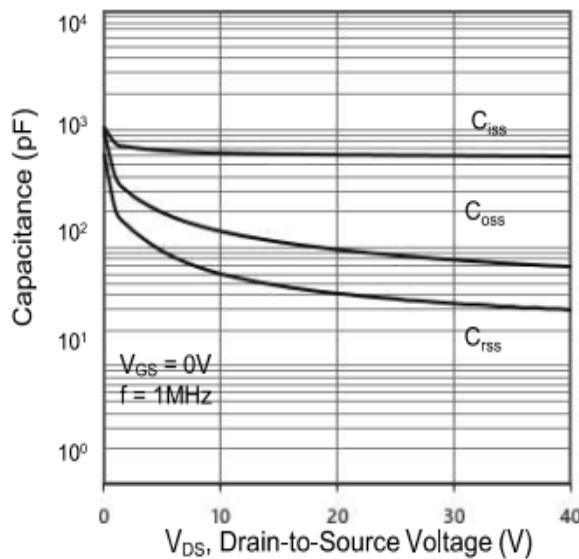


**Figure 6. On-Resistance vs. Temperature**

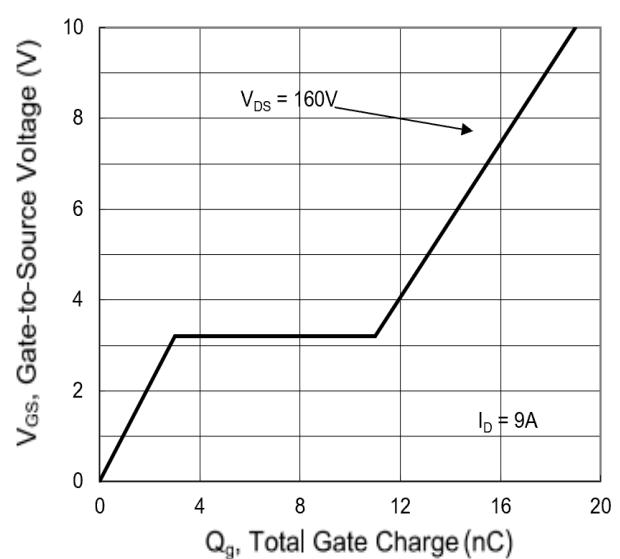


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

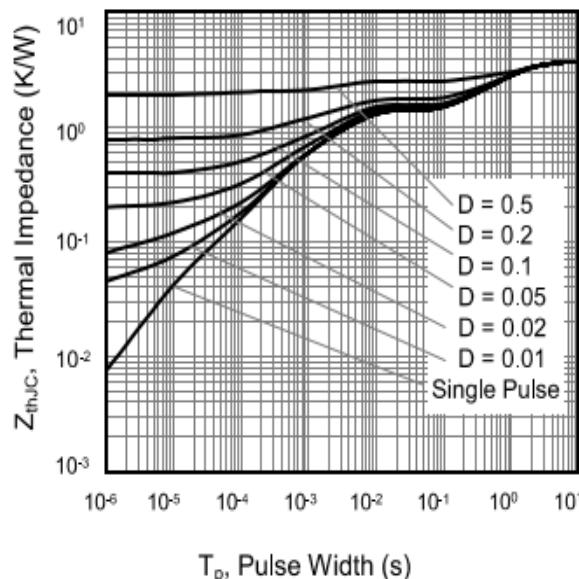
**Figure 7. Capacitance**



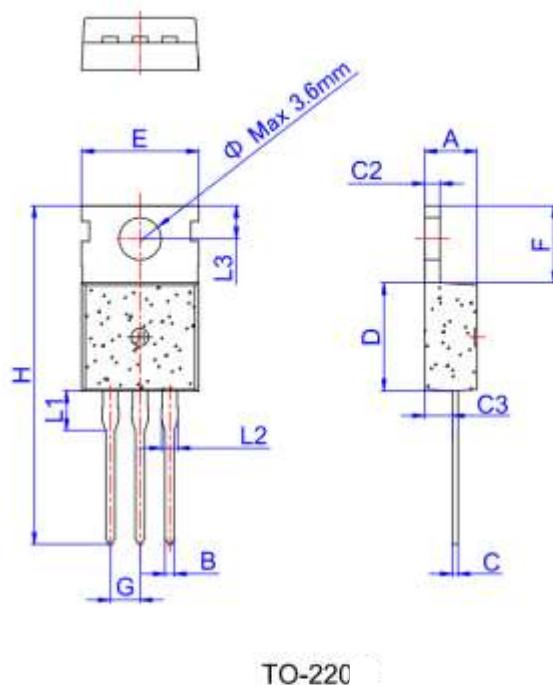
**Figure 8. Gate Charge**



**Figure 9. Transient Thermal Impedance**  
TO-220



### TO-220 Package Information



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.70		0.90	0.028		0.035
C	0.45		0.60	0.018		0.024
C2	1.23		1.32	0.048		0.052
C3	2.20		2.60	0.087		0.102
D	8.90		9.90	0.350		0.390
E	9.90		10.3	0.390		0.406
F	6.30		6.90	0.248		0.272
G		2.54			0.1	
H	28.0		29.8	1.102		1.173
L1		3.39			0.133	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
Ø		3.6			0.142	

## Revision History

Revision	Release	Remark
V1.0	2023/06/26	Initial Release

## Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Allpower assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.