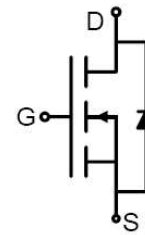


## Feature

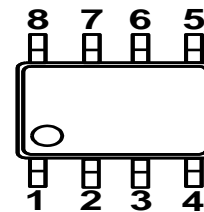
- 100V, 8A  
 $R_{DS(ON)} < 25m\Omega @ V_{GS}=10V$  (TYP:18m $\Omega$ )  
 $R_{DS(ON)} < 38m\Omega @ V_{GS}=4.5V$  (TYP:25m $\Omega$ )
- Split Gate Trench Technology
- Lead free product is acquired
- Excellent  $R_{DS(ON)}$  and Low Gate Charge



Schematic Diagram

## Application

- PWM applications
- Load Switch
- Power management



SOP-8

## Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
G250N01S	APG250N01S	SOP-8	13 inch	-	4000

## ABSOLUTE MAXIMUM RATINGS ( $T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current ( $T_c=25^\circ\text{C}$ )	$I_D$	8	A
Continuous Drain Current ( $T_c=100^\circ\text{C}$ )	$I_D$	5	A
Pulsed Drain Current <sup>(1)</sup>	$I_{DM}$	40	A
Single Pulsed Avalanche Energy <sup>(2)</sup>	$E_{AS}$	10	mJ
Power Dissipation	$P_D$	45	W
Thermal Resistance from Junction to Case	$R_{\theta JC}$	2.5	$^\circ\text{C/W}$
Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55~ +150	$^\circ\text{C}$

**MOSFET ELECTRICAL CHARACTERISTICS**( $T_a=25^{\circ}\text{C}$  unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Static Characteristics</b>						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100	-	-	V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 100V, V_{GS} = 0V$	-	-	1	$\mu A$
Gate-body leakage current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	$\pm 100$	nA
Gate threshold voltage <sup>(3)</sup>	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.2	1.8	2.8	V
Drain-source on-resistance <sup>(3)</sup>	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 6A$	-	18	25	m $\Omega$
		$V_{GS} = 4.5V, I_D = 5A$	-	25	38	m $\Omega$
Forward Threshold Voltage	$g_{fs}$	$V_{DS} = 10V, I_D = 6A$	-	22	-	S
Gate Resistance	$R_g$	$V_{DS} = V_{GS} = 0V, f = 1MHz$	-	1.62	-	$\Omega$
<b>Dynamic characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 50V, V_{GS} = 0V, f = 1MHz$	-	822	-	pF
Output Capacitance	$C_{oss}$		-	310	-	
Reverse Transfer Capacitance	$C_{rss}$		-	23.5	-	
<b>Switching characteristics</b>						
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 50V, I_D = 6A,$ $V_{GS} = 10V, R_G = 3\Omega$	-	15	-	ns
Turn-on rise time	$t_r$		-	3.2	-	
Turn-off delay time	$t_{d(off)}$		-	30	-	
Turn-off fall time	$t_f$		-	7.6	-	
Total Gate Charge	$Q_g$	$V_{DS} = 50V, I_D = 6A,$ $V_{GS} = 10V$	-	22.7	-	nC
Gate-Source Charge	$Q_{gs}$		-	6.2	-	
Gate-Drain Charge	$Q_{gd}$		-	5.3	-	
Reverse Recovery Charge	$Q_{rr}$	$I_F = 6A, di/dt = 100A/\mu s$		59		nC
Reverse Recovery Time	$T_{rr}$	$I_F = 6A, di/dt = 100A/\mu s$		45		ns
<b>Source-Drain Diode characteristics</b>						
Diode Forward voltage <sup>(3)</sup>	$V_{DS}$	$V_{GS} = 0V, I_S = 6A$	-	-	1.2	V
Diode Forward current <sup>(4)</sup>	$I_S$		-	-	8	A

**Notes:**

1. Repetitive Rating: pulse width limited by maximum junction temperature
2. EAS Condition:  $T_J = 25^{\circ}\text{C}, V_{DD} = 50V, R_G = 25\Omega, L = 0.5\text{mH}$
3. Pulse Test: pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$
4. Surface Mounted on FR4 Board,  $t \leq 10\text{ sec}$

### Typical Performance Characteristics

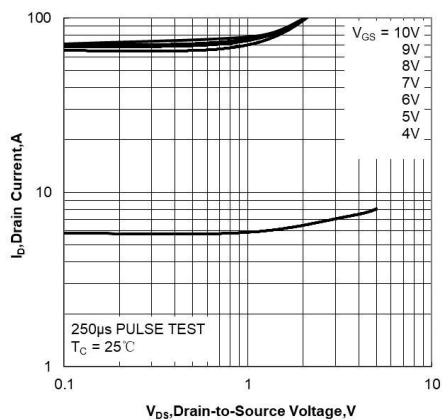


Figure 1. Output Characteristics

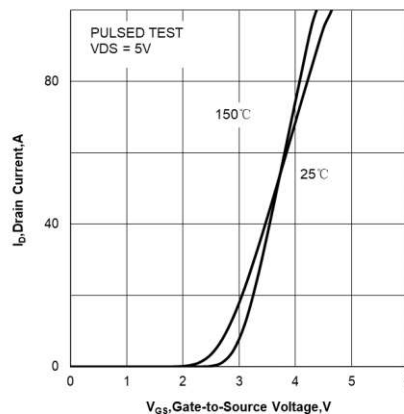


Figure 2. Transfer Characteristics

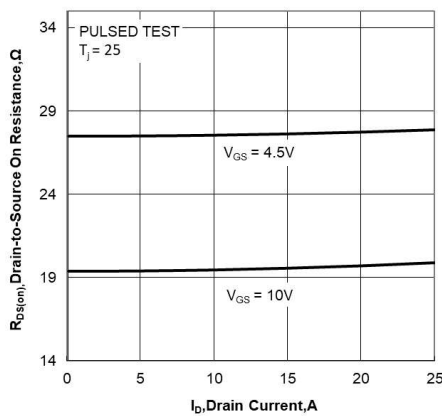


Figure 3. Drain-to-Source On Resistance vs Drain Current

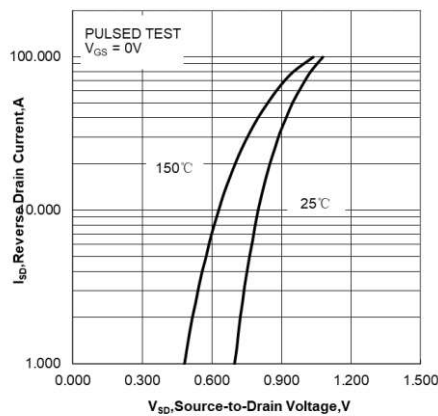


Figure 4. Body Diode Forward Voltage vs Source Current and Temperature

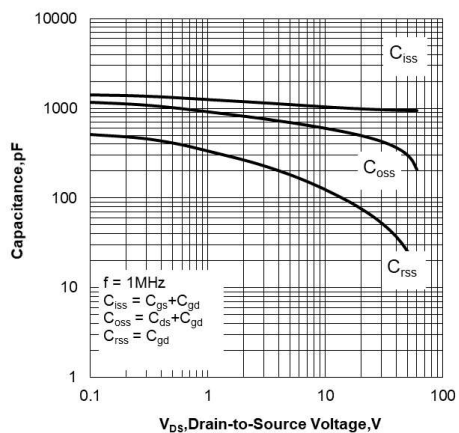


Figure 5. Capacitance Characteristics

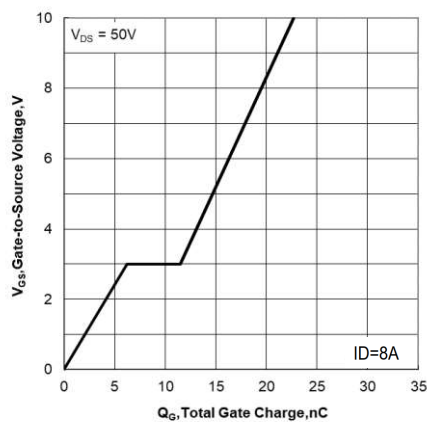


Figure 6. Gate Charge Characteristics

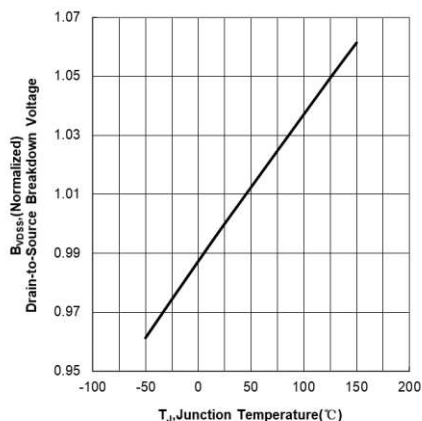


Figure 7. Normalized Breakdown Voltage vs Junction Temperature

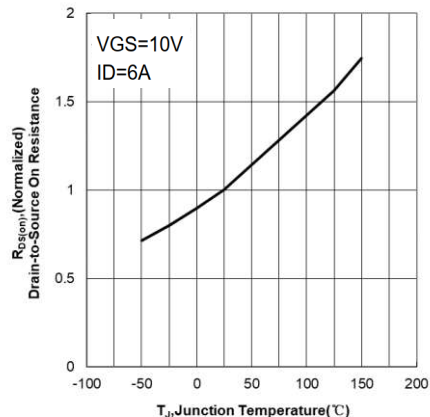


Figure 8. Normalized On Resistance vs Junction Temperature

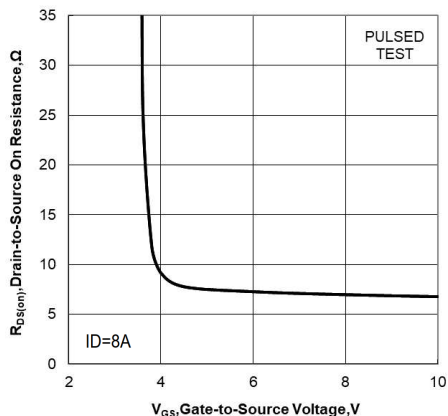


Figure 9. Drain-to-Source On Resistance vs Gate Voltage and Drain Current

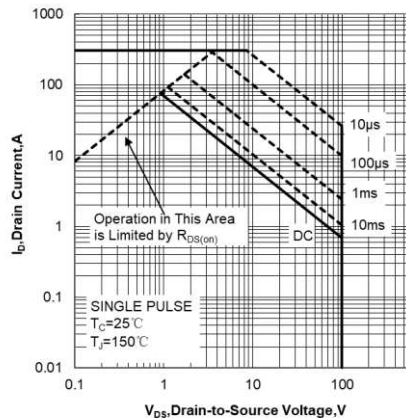


Figure 10. Maximum Safe Operating Area

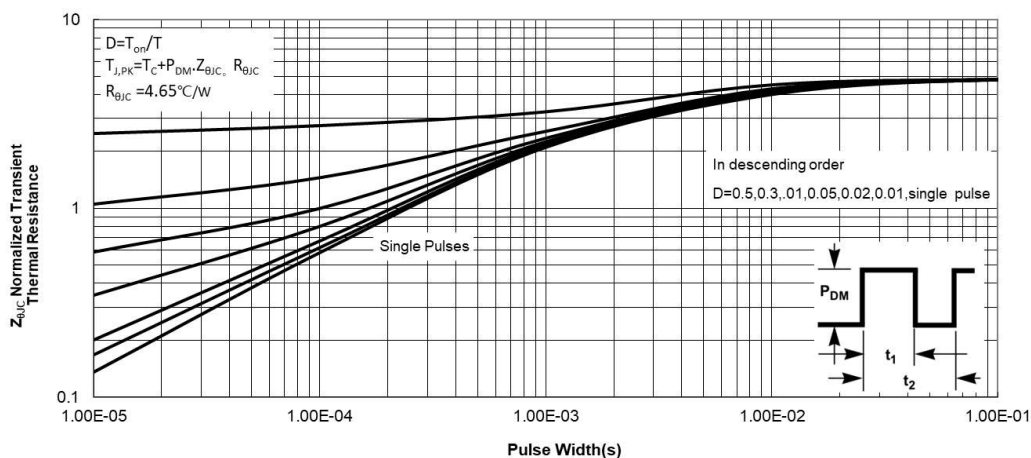
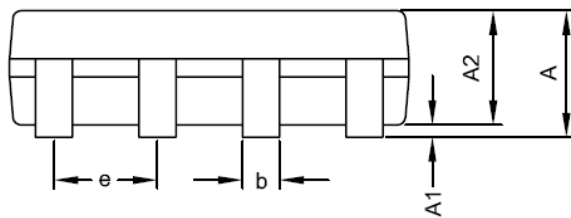
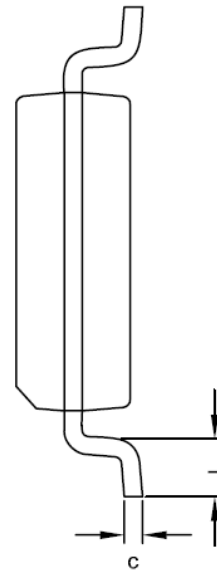
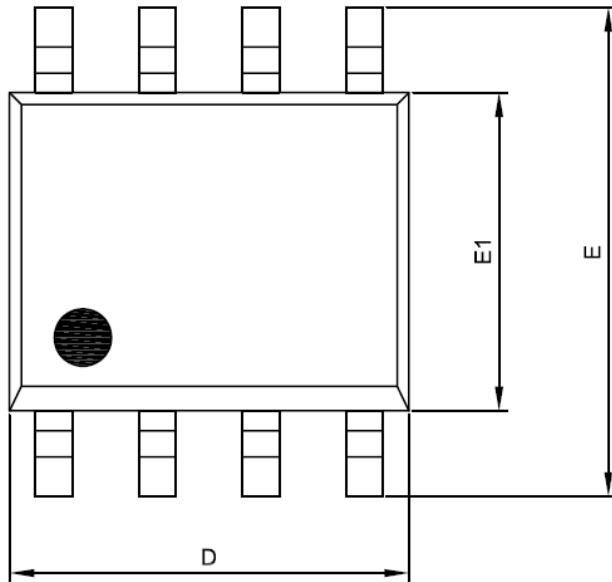


Figure 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

## Package Dimensions

SOP- 8



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	1.35	1.75
A1	0.00	0.25
A2	1.15	1.50
D	4.80	5.00
E	5.80	6.20
E1	3.80	4.00
c	0.19	0.27
b	0.33	0.53
e	1.27 BSC	
L	0.40	1.27

## Revision History

Revision	Release	Remark
V1.0	2022/12/15	Initial Release

## Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Allpower assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.