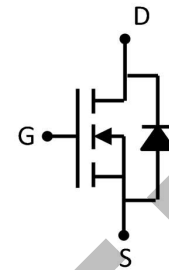


Features

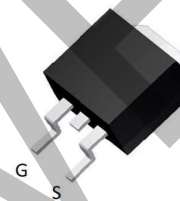
- 120V,100A
 $R_{DS(on)} < 6.0m\ \Omega @ V_{GS}=10V$ TYP:5.5m Ω
 $R_{DS(on)} < 8.0m\ \Omega @ V_{GS}=6V$ TYP:6.9m Ω
- Advanced trench cell design
- Low Thermal Resistance

Applications

- Motor drivers
- DC - DC Converter



Schematic Diagram



TO-263 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
G060N12D	APG060N12D	TO-263	-	-	800

ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	120	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_a=25^\circ\text{C}$)	I_D	100	A
Pulsed Drain Current ^(1,2,3)	I_{DM}	240	A
Single Pulsed Avalanche Energy ⁽¹⁾	E_{AS}	800	mJ
Power Dissipation	P_D	147	W
Thermal Resistance from Junction to Case ⁽¹⁾	$R_{\theta JC}$	0.85	$^\circ\text{C/W}$
Thermal Resistance- Junction to Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55~ +150	$^\circ\text{C}$

MOSFET ELECTRICAL CHARACTERISTICS(T_a=25°C unless otherwise noted)

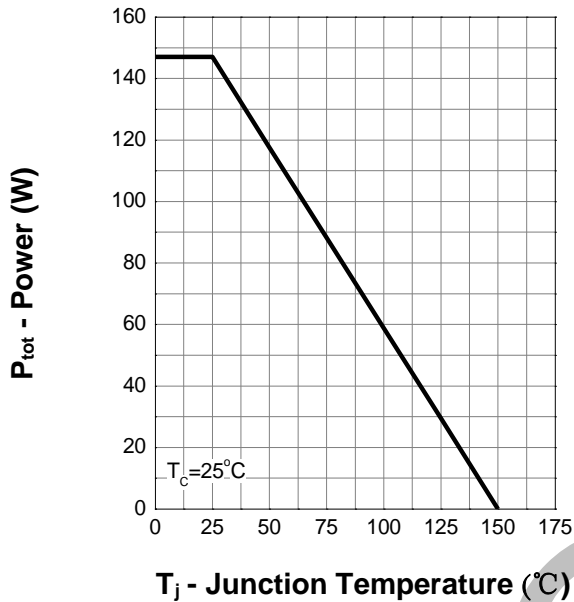
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D =250μA	120	-	-	V
Zero gate voltage drain current	I _{DSS}	V _{DS} =96V, V _{GS} = 0V	-	-	1	μA
Gate-body leakage current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2.0	-	4.0	V
Drain-source on-resistance ⁽²⁾	R _{DS(on)}	V _{GS} =10V, I _D =50A	-	5.5	6.0	mΩ
		V _{GS} =6V, I _D =30A	-	6.9	8.0	mΩ
Dynamic characteristics						
Input Capacitance	C _{iss}	V _{DS} =60V, V _{GS} =0V, f =1.0MHz	-	4903	-	pF
Output Capacitance	C _{oss}		-	566	-	
Reverse Transfer Capacitance	C _{rss}		-	47	-	
Switching characteristics						
Turn-on delay time	t _{d(on)}	V _{DD} =60V, I _D =50A, R _G =3.9Ω	-	18	-	ns
Turn-on rise time	t _r		-	71	-	
Turn-off delay time	t _{d(off)}		-	51	-	
Turn-off fall time	t _f		-	79	-	
Total Gate Charge	Q _g	V _{DS} =60V, I _D =50A, V _{GS} =10V	-	81	-	nC
Gate-Source Charge	Q _{gs}		-	28	-	
Gate-Drain Charge	Q _{gd}		-	18	-	
Source-Drain Diode characteristics						
Diode Forward voltage ⁽²⁾	V _{SD}	T _J =25°C, V _{GS} =0V, I _S =50A	-	-	1.3	V
Diode Forward current	I _S	T _C =25°C	-	-	100	A
Body Diode Reverse Recovery Time	t _{rr}	T _J =25°C, I _F =50A, di/dt=100A/us		100		ns
Body Diode Reverse Recovery Charge	Q _{rr}	T _J =25°C, I _F =50A, di/dt=100A/us		307		uc

Notes:

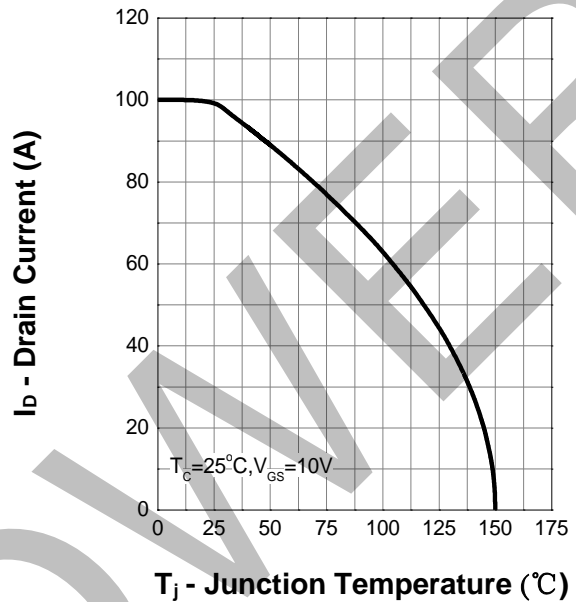
1. Surface Mounted on 1 in² pad area, t ≤ 10 sec
2. Pulse width ≤ 300 μs, duty cycle ≤ 2 %
3. Limited by bonding wire

Typical Characteristics

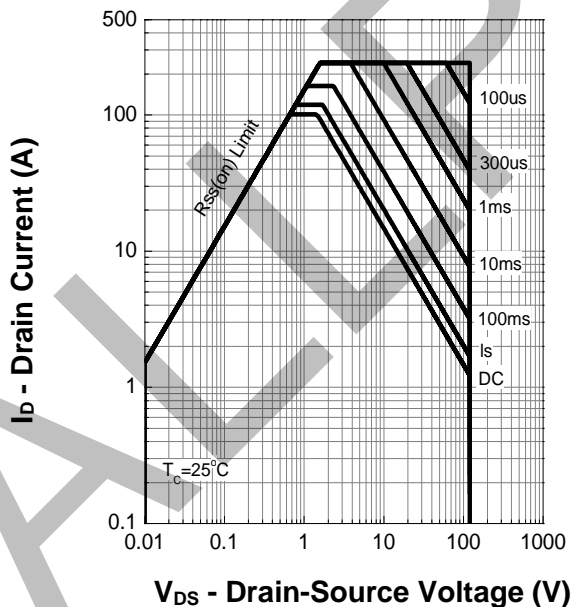
Power Capability



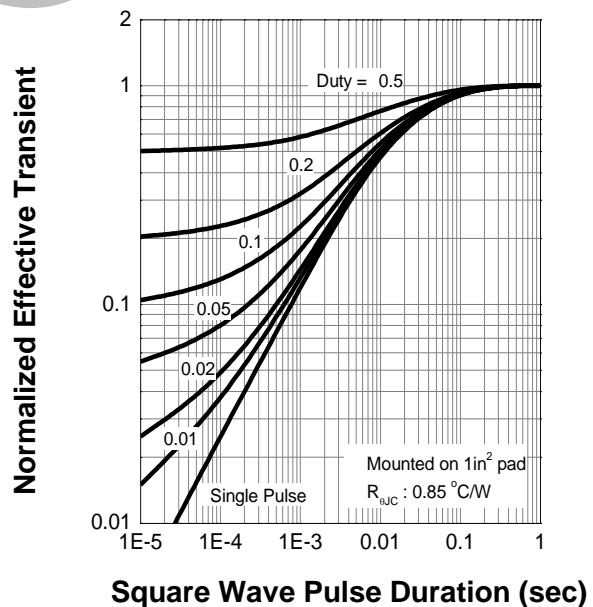
Current Capability



Safe Operating Area

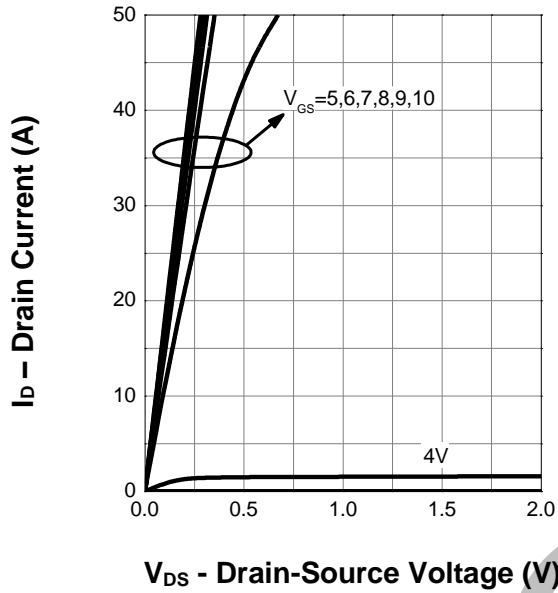


Thermal Transient Impedance

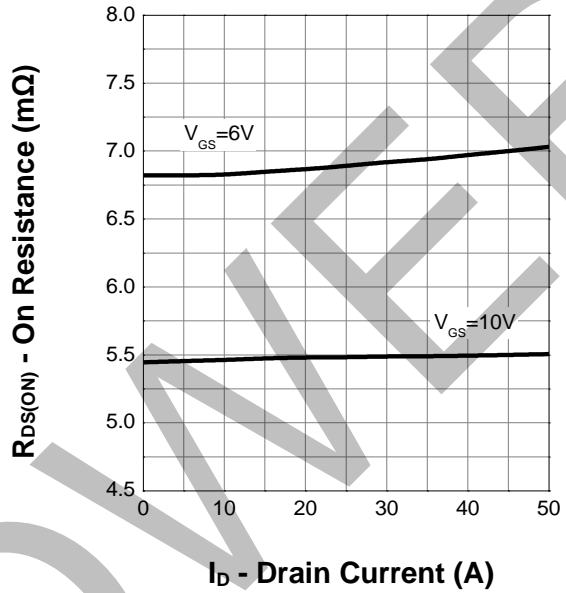


Typical Characteristics (cont.)

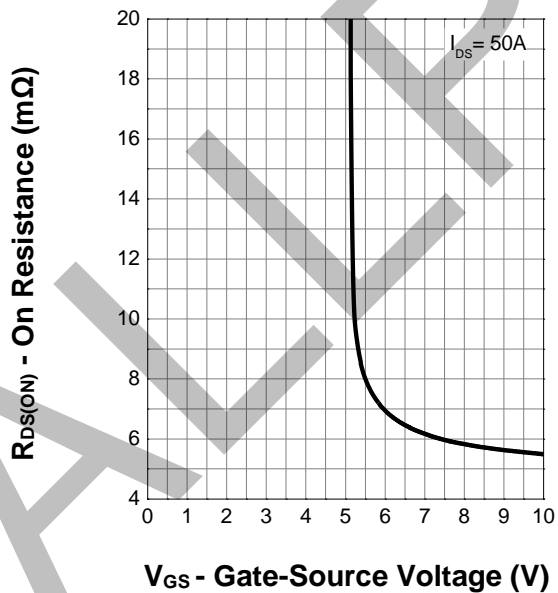
Output Characteristics



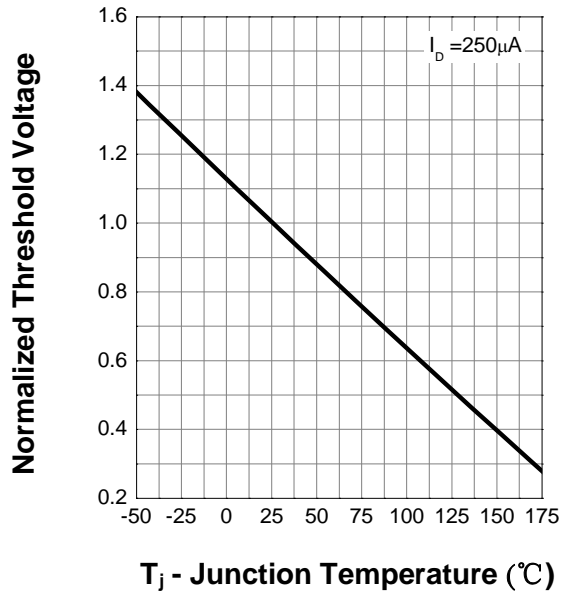
Drain-Source On Resistance



Transfer Characteristics

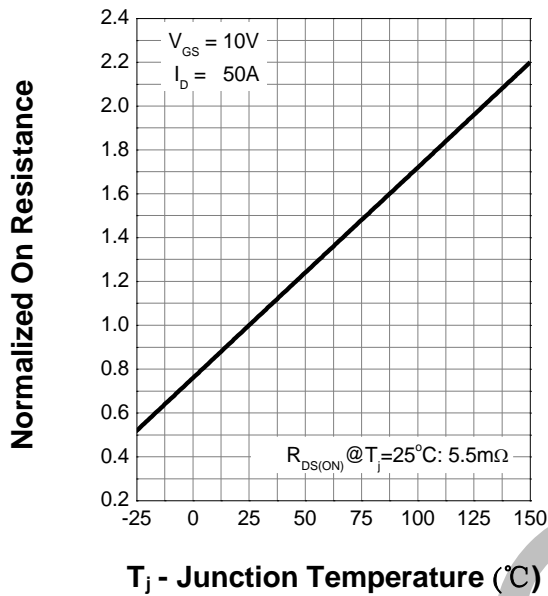


Gate Threshold Voltage

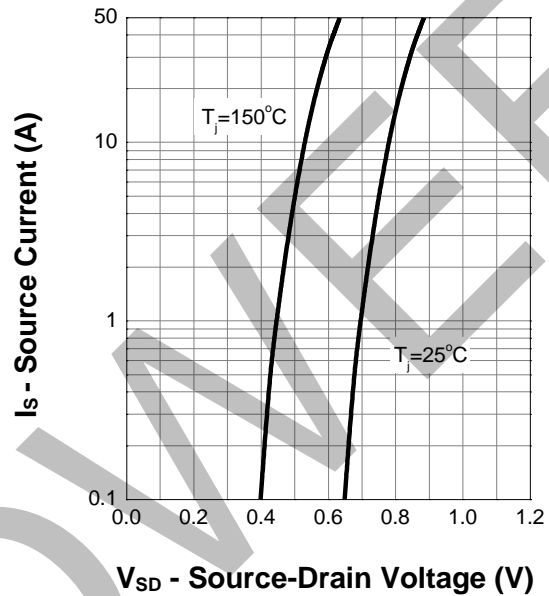


Typical Characteristics (cont.)

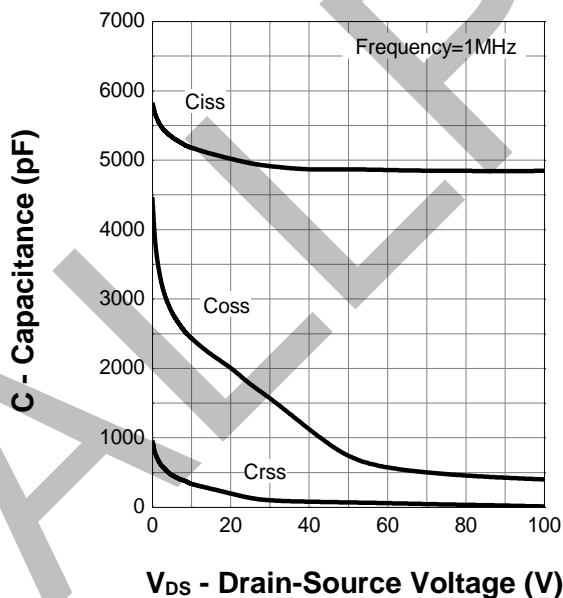
Drain-Source On Resistance



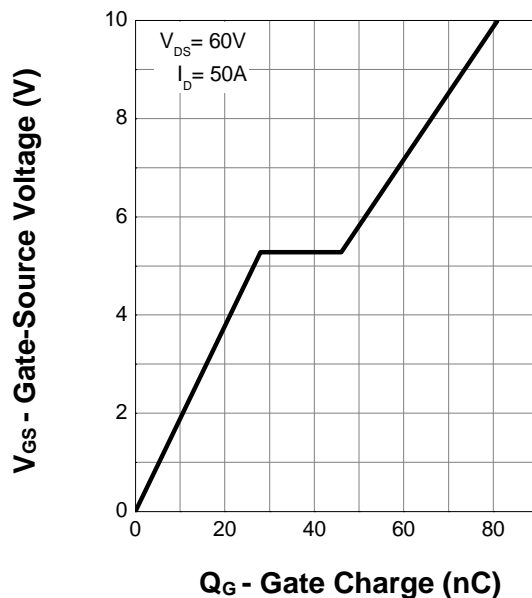
Body Diode Characteristics



Capacitance



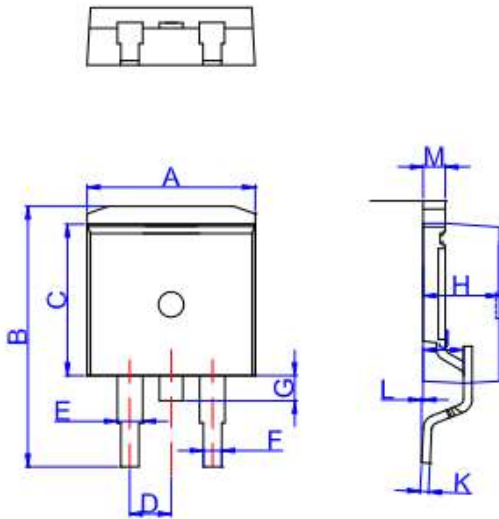
Gate Charge



APG060N12D

N-Channel Enhancement Mosfet

TO-263 Package Information



TO-263

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.90		10.20	0.390		0.402
B	14.70		15.80	0.579		0.622
C	9.4		9.6	0.37		0.378
D		2.54			0.100	
E	1.20		1.40	0.047		0.055
F	0.75		0.85	0.029		0.033
G			1.75			0.069
H	4.40		4.70	0.173		0.185
J	2.30		2.70	0.091		0.106
K	0.38		0.55	0.015		0.022
L	0	0.10	0.25	0	0.004	0.010
M	1.25		1.35	0.049		0.053