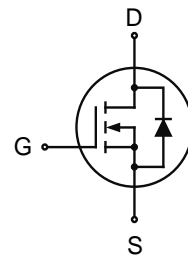


Feature

- 650V,20 A
RDS(ON) $\leq 0.45 \Omega$ @ VGS=10V, TYP=0.35 Ω
- Fast Switching
- Low ON Resistance(Rdson $\leq 0.45\Omega$)
- Low Gate Charge (Typical Data:57nC)
- 100% Single Pulse avalanche energy Test
- Halogen Free



Schematic Diagram



TO-220F

Application

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
20N65F	AP20N65F	TO-220F	-	-	1000

ABSOLUTE MAXIMUM RATINGS (T_J=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	650	V
Gate-Source Voltage	V _{GS}	±30	V
Continuous Drain Current (T _C =25°C)	I _D	20	A
Continuous Drain Current (T _C =100°C)	I _D	12.6	A
Pulsed Drain Current ⁽¹⁾	I _{DM}	80	A
Power Dissipation	P _D	83	W
Single Pulse Avalanche Energy ⁽²⁾	E _{AS}	550	mJ
Junction to case ⁽⁴⁾	R _{θJC}	1.5	°C/W
Junction to Ambient ⁽⁴⁾	R _{θJA}	65	°C/W
Junction Temperature	T _J	150	°C
Storage Temperature	T _{STG}	-55~ +150	°C

MOSFET ELECTRICAL CHARACTERISTICS($T_J=25^{\circ}\text{C}$ unless otherwise noted)

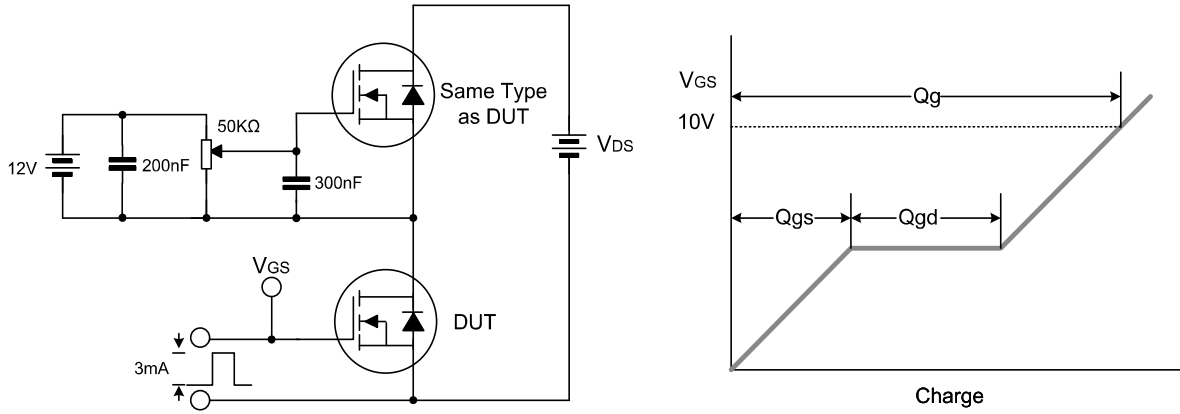
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	650	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 650V, V_{GS} = 0V, T_J = 25^{\circ}\text{C}$	-	-	1	μA
Gate-body leakage current	I_{GSS}	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	± 100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.0	-	4	V
Drain-source on-resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 10A$	-	0.35	0.45	Ω
Dynamic characteristics						
Input Capacitance	C_{iss}	$V_{DS} = 25V, V_{GS} = 0V, f = 1\text{MHz}$	-	2890	-	pF
Output Capacitance	C_{oss}		-	280	-	
Reverse Transfer Capacitance	C_{rss}		-	11	-	
Forward Transconductance	G_{fs}	$V_{DS} = 15V, I_D = 10A$	-	18	-	S
Switching characteristics						
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 350V, I_D = 20A,$ $V_{GS} = 10V, R_G = 25\Omega$	-	44	-	ns
Turn-on rise time	t_r		-	64	-	
Turn-off delay time	$t_{d(off)}$		-	170	-	
Turn-off fall time	t_f		-	73	-	
Total Gate Charge	Q_g	$V_{DS} = 560V, I_D = 20A,$ $V_{GS} = 10V$	-	57	-	nC
Gate-Source Charge	Q_{gs}		-	18	-	
Gate-Drain Charge	Q_{gd}		-	18.5	-	
Source-Drain Diode characteristics						
Diode Forward voltage	V_{SD}	$V_{GS} = 0V, I_S = 10A$	-	-	1.4	V
Diode Forward current	I_S		-	-	20	A
Body Diode Reverse Recovery Time	t_{rr}	$V_{GS} = 0V, I_F = 20A,$		680		ns
Body Diode Reverse Recovery Charge	Q_{rr}	$dI/dt = 100A/\mu s$		9.3		μC

Notes:

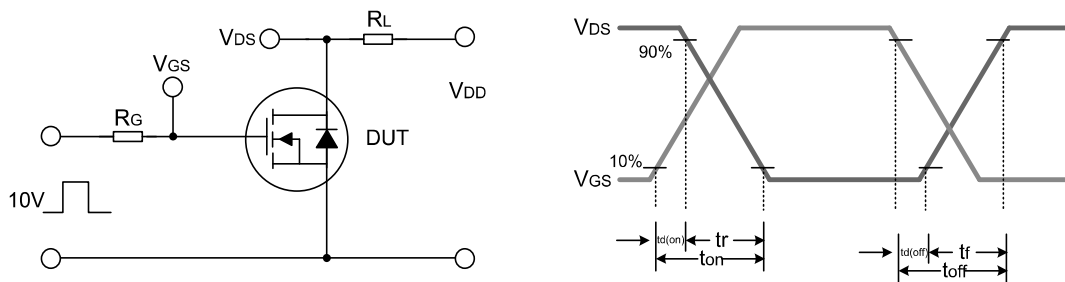
1. Repetitive Rating:pulse width limited by maximum junction temperature.
2. $L=10\text{mH}, R_g=25\Omega, I_{AS}=10.5A$, starting $T_J=25^{\circ}\text{C}$.
3. $I_{SD}=20A, dI/dt \leq 100A/\mu s, V_{DD} \leq BV_{DSS}$, starting $T_J=25^{\circ}\text{C}$.
4. Repetitive rating; pulse width limited by maximum junction tempera

Test Circuit

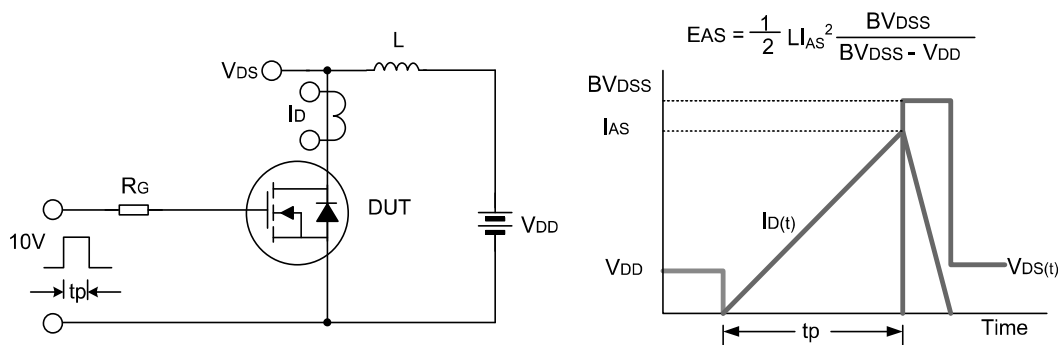
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveform



Typical Performance Characteristics

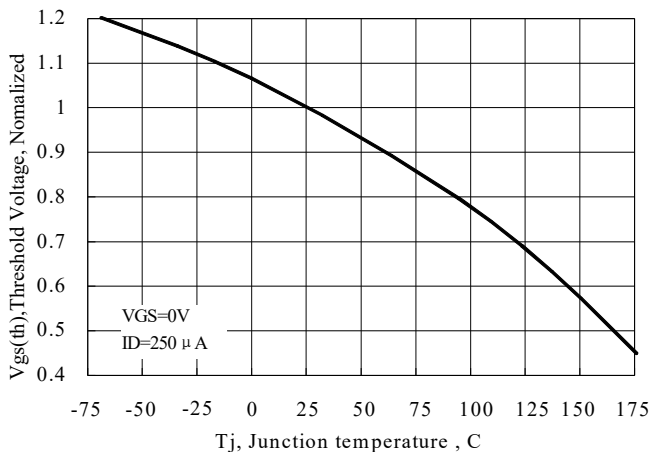


Figure 1 Typical Theshold Voltage vs Junction Temperature

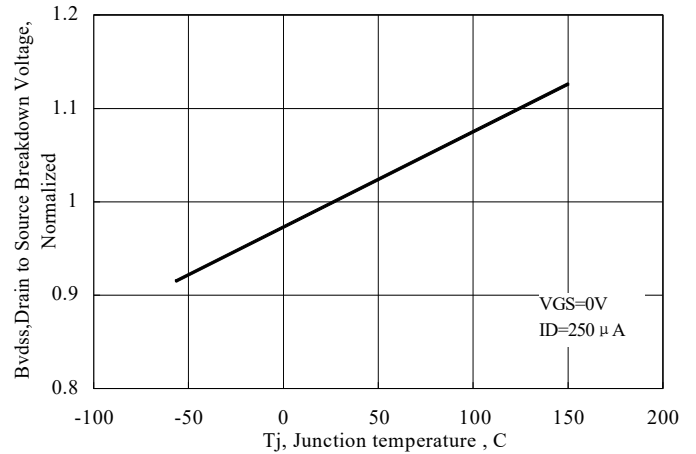


Figure 2 Typical Breakdown Voltage vs Junction Temperature

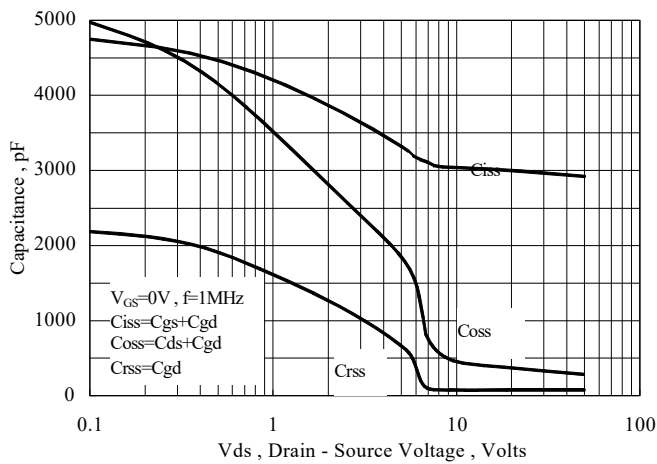


Figure 3 Typical Capacitance vs Drain to Source Voltage

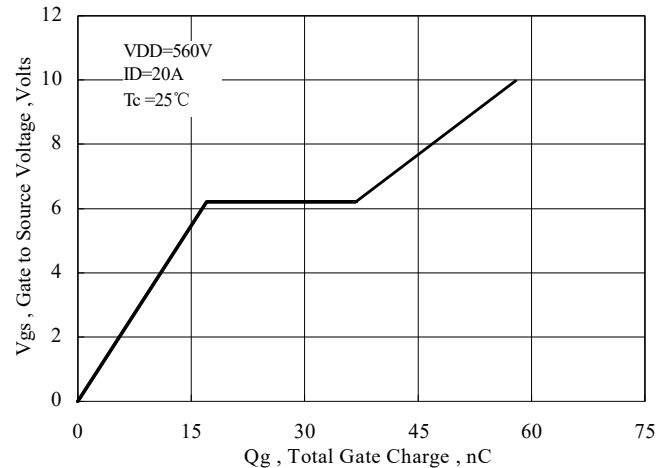


Figure 4 Typical Gate Charge vs Gate to Source Voltage

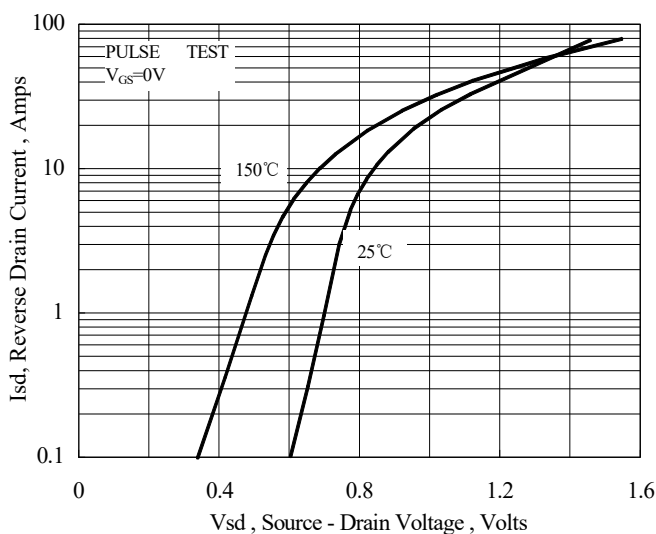


Figure 5 Typical Body Diode Transfer Characteristics

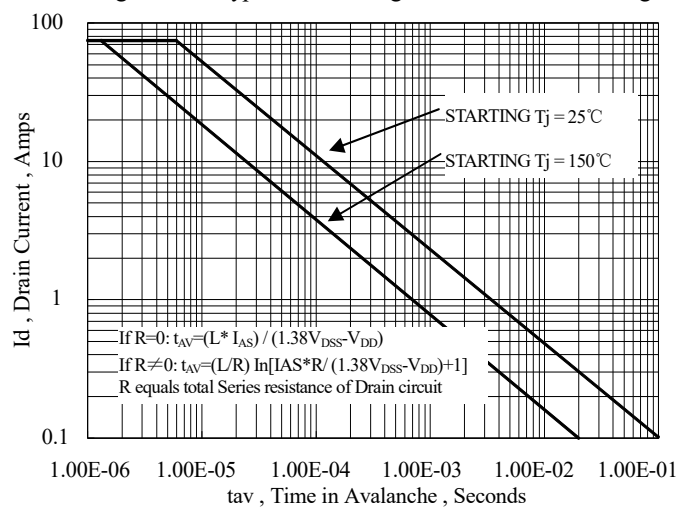


Figure 6 Unclamped Inductive Switching Capability

Typical Performance Characteristics

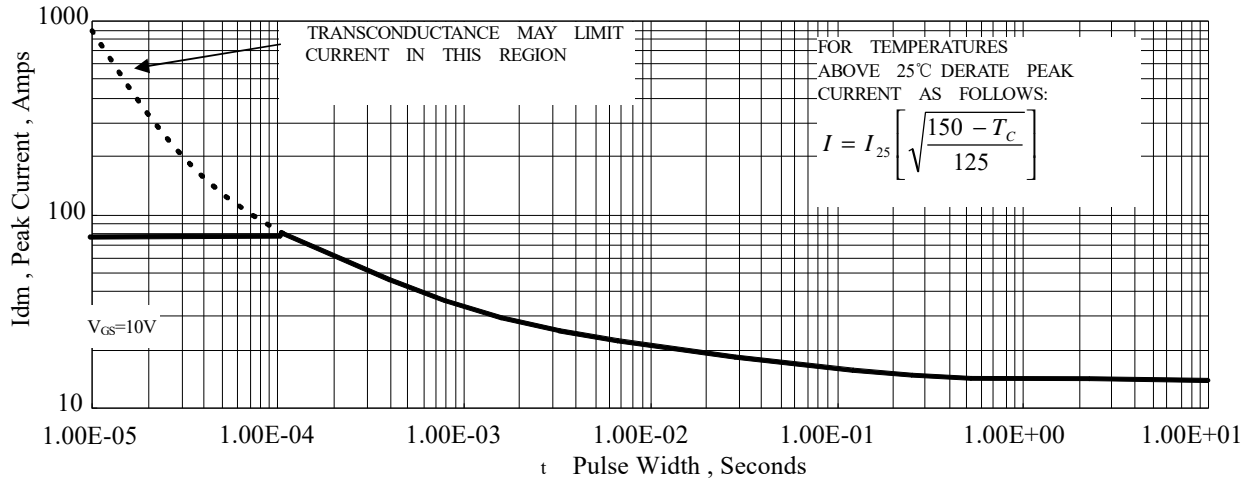


Figure 7 Maximum Peak Current Capability

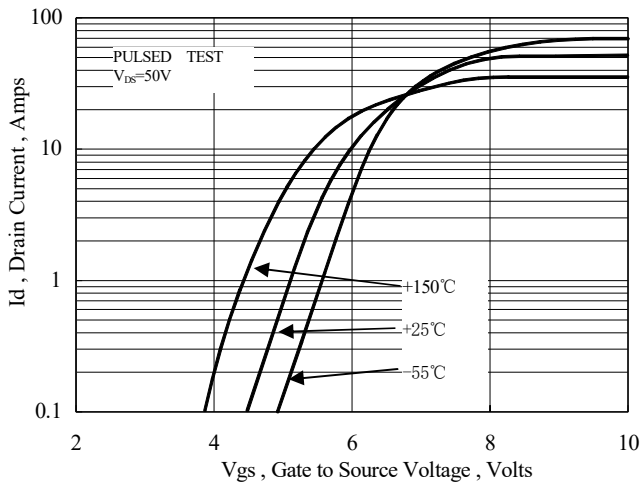


Figure 8 Typical Transfer Characteristics

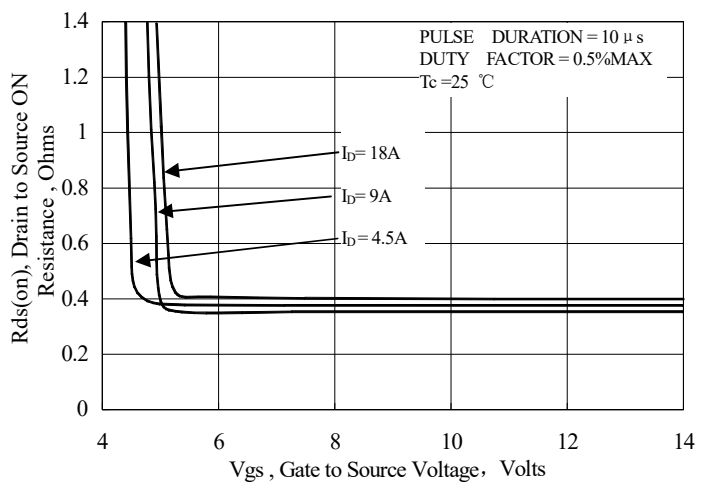


Figure 9 Typical Drain to Source ON Resistance vs Gate Voltage and Drain Current

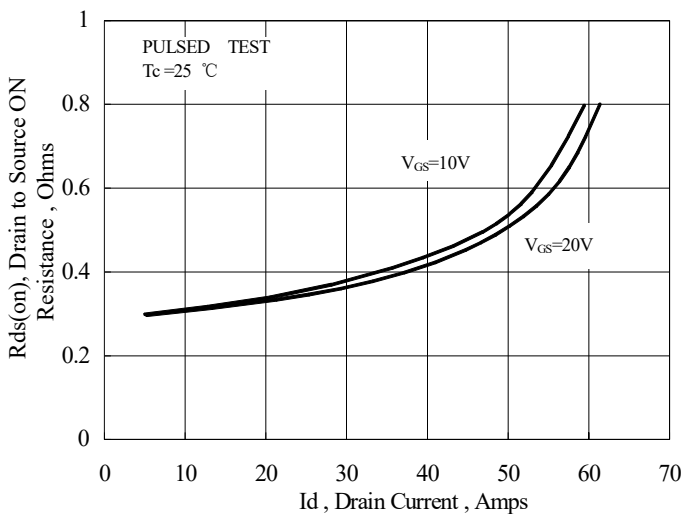


Figure 10 Typical Drain to Source ON Resistance vs Drain Current

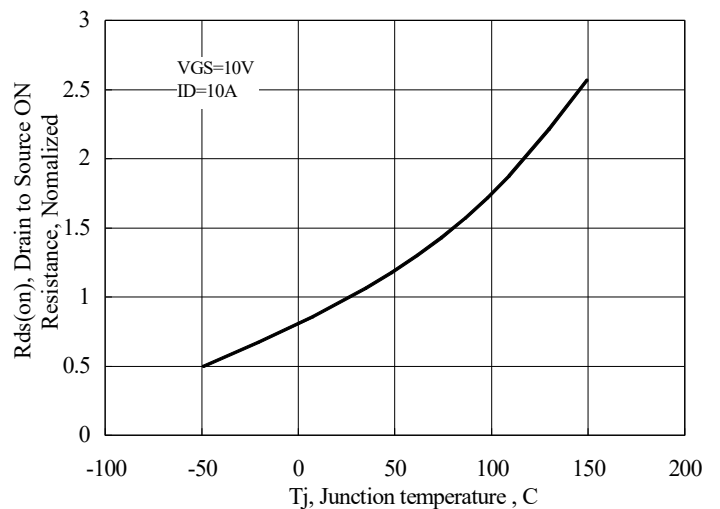


Figure 11 Typical Drain to Source ON Resistance vs Junction Temperature

Typical Performance Characteristics

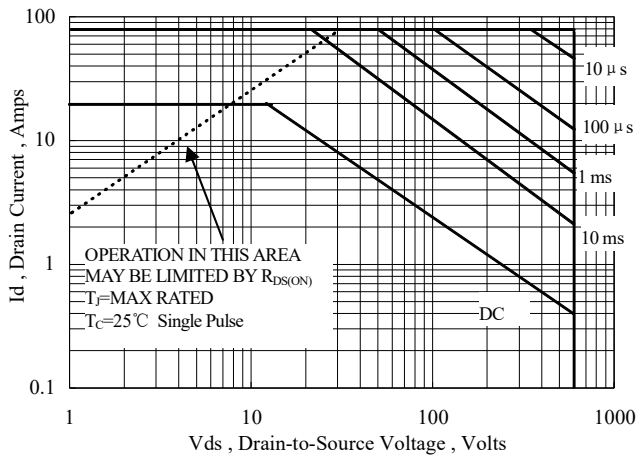


Figure 12 Maximum Forward Bias Safe Operating Area

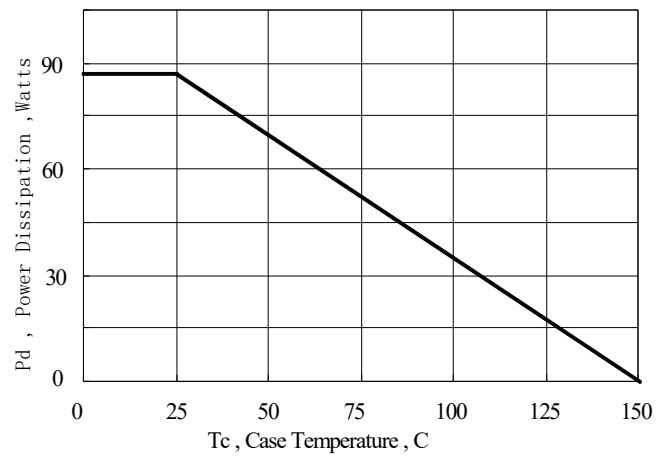


Figure 13 Maximum Power Dissipation vs Case Temperature

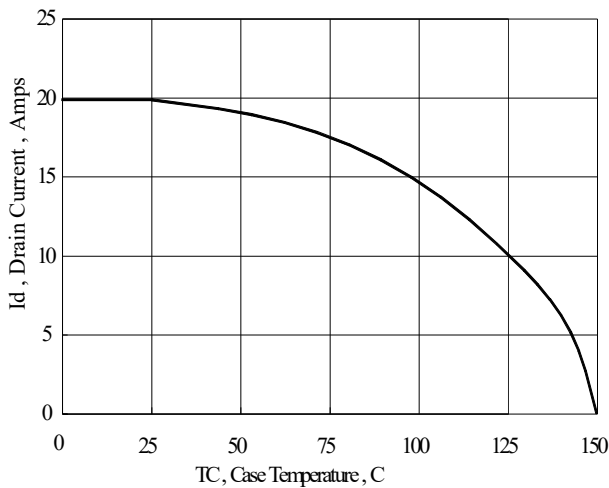


Figure 14 Maximum Continuous Drain Current vs Case Temperature

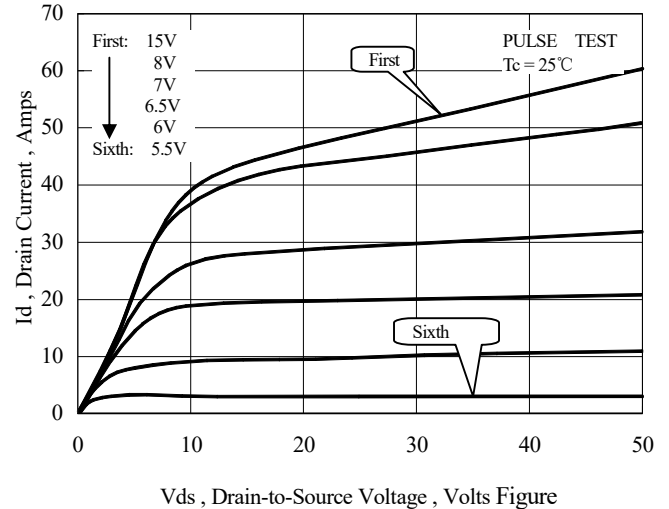


Figure 15 Typical Output Characteristics

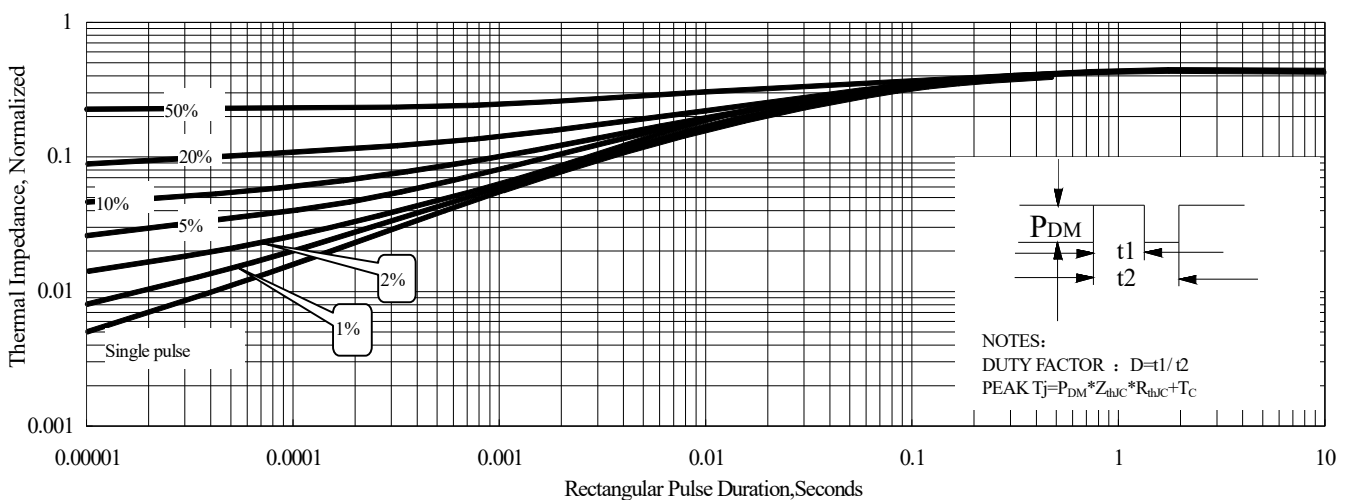
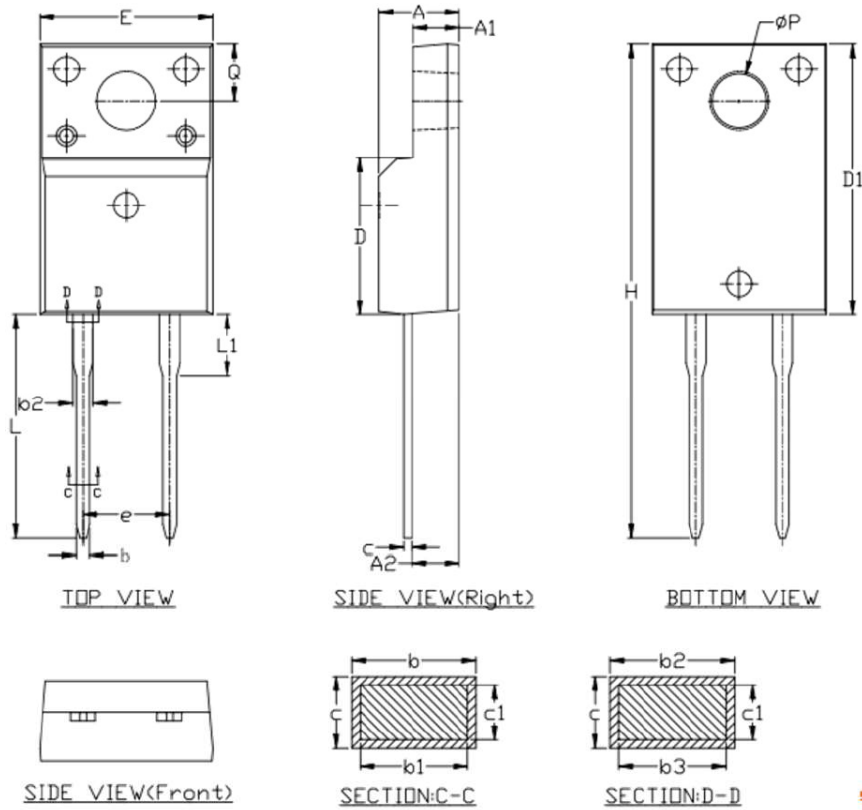


Figure 16 Maximum Effective Thermal Impedance, Junction to Case

Package Dimensions of TO-220F

Note: UNIT: mm



DIM SYMBOL	MIN.	NOM.	MAX.
A	4.600	4.700	4.800
A1	2.600	2.700	2.800
A2	2.660	2.760	2.860
b	0.740	0.840	0.940
b1	0.700	0.800	0.900
b2	1.140	1.240	1.340
b3	1.100	1.200	1.300
c	0.440	0.540	0.640
c1	0.400	0.500	0.600
D	9.090	9.190	9.290
D1	15.770	15.870	15.970
E	10.060	10.160	10.260
e	5.080 BSC.		
H	28.800	29.000	29.200
L	12.930	13.130	13.330
L1	3.400	3.600	3.800
ϕP	3.080	3.180	3.280
Q	3.150	3.350	3.550

Revision History

Revision	Release	Remark
V1.0	2023/10/07	Initial Release

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Allpower assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.