

APG030N08G

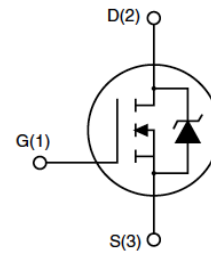
N-Channel Enhancement Mosfet

AIPOWER

DATA SHEET

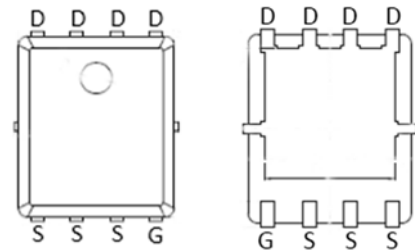
Features

- 80V,150A
 $R_{DS(ON)} < 3.0m\ \Omega @ V_{GS}=10V$ TYP:2.5m Ω
 $R_{DS(ON)} < 4.0m\ \Omega @ V_{GS}=6V$ TYP:3.5m Ω
- Surface-mounted package
- Split Gate Trench Technology



Applications

- Power appliances
- BMS appliances
- High power inverter system



PDFN5X6

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
G030N08G	APG030N08G	PDFN5X6	-	-	5000

ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	80	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_a=25^\circ\text{C}$) ^(2,3)	I_D	150	A
Pulsed Drain Current ^(1,2,3)	I_{DM}	240	A
Single Pulsed Avalanche Energy ($V_{DD}=50V, L=0.1mH$) ⁽²⁾	E_{AS}	420	mJ
Drain Power Dissipation	P_D	156	W
Thermal Resistance from Junction to Case ⁽²⁾	$R_{\theta JC}$	0.8	$^\circ\text{C/W}$
Thermal Resistance- Junction to Ambient ⁽²⁾	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55~ +150	$^\circ\text{C}$

Notes:

1. Pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2\ \%$
2. Surface Mounted on n 1 in2 pad area, $t \leq 10\ \text{sec}$.
3. Limited by bonding wire

MOSFET ELECTRICAL CHARACTERISTICS(T_a=25°C unless otherwise noted)

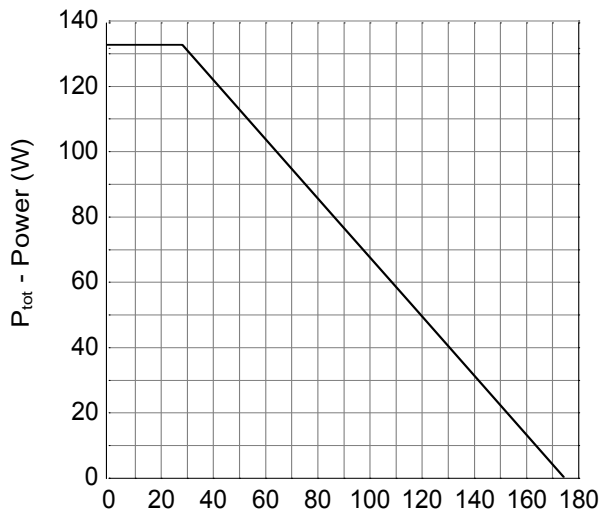
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D =250μA	80	-	-	V
Zero gate voltage drain current	I _{DSS}	V _{DS} =64V, V _{GS} = 0V	-	-	1	μA
Gate-body leakage current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2.0	-	4.0	V
Drain-source on-resistance ^(a)	R _{DS(on)}	V _{GS} =10V, I _D =30A	-	2.5	3.0	mΩ
		V _{GS} =6V, I _D =20A		3.5	4.0	mΩ
Dynamic characteristics						
Input Capacitance	C _{iss}	V _{DS} =40V, V _{GS} =0V, f =1.0MHz	-	5591	-	pF
Output Capacitance	C _{oss}		-	744	-	
Reverse Transfer Capacitance	C _{rss}		-	75	-	
Switching characteristics						
Turn-on delay time	t _{d(on)}	V _{DD} =40V, I _D =30A, R _G =4.5Ω, R _L =1.3Ω, V _G =10V	-	23	-	ns
Turn-on rise time	t _r		-	65	-	
Turn-off delay time	t _{d(off)}		-	71	-	
Turn-off fall time	t _f		-	73	-	
Total Gate Charge	Q _g	V _{DS} =40V, I _D =30A, V _{GS} =10V	-	101	-	nC
Gate-Source Charge	Q _{gs}		-	28	-	
Gate-Drain Charge	Q _{gd}		-	25	-	
Source-Drain Diode characteristics						
Diode Forward voltage ^(a)	V _{SD}	T _J =25°C, V _{GS} =0V, I _S =30A	-	-	1.3	V
Diode Forward current	I _S	T _C =25°C	-	-	150	A
Body Diode Reverse Recovery Time	t _{rr}	T _J =25°C, I _F =30A, di/dt=100A/us		62		ns
Body Diode Reverse Recovery Charge	Q _{rr}	T _J =25°C, I _F =30A, di/dt=100A/us		83		uc

Notes:

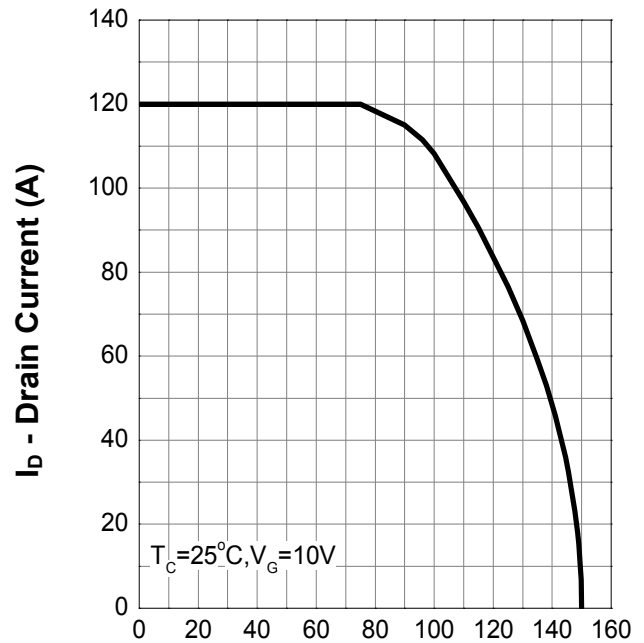
- a) Pulse width ≤ 300 μs, duty cycle ≤ 2%
- b) Guaranteed by design, not subject to production testing

Typical Characteristics (cont.)

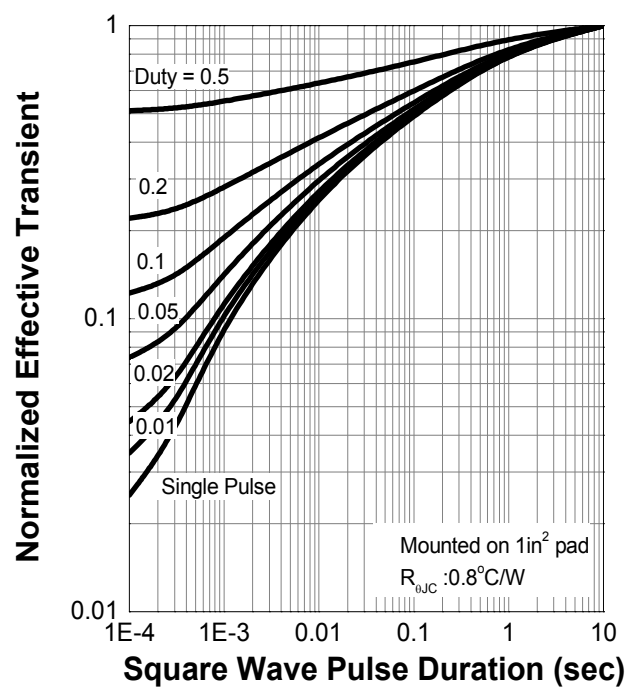
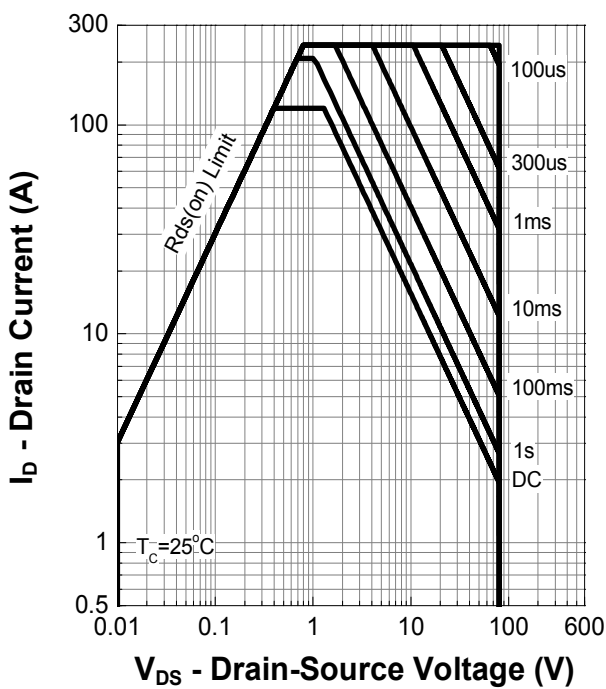
Power Capability



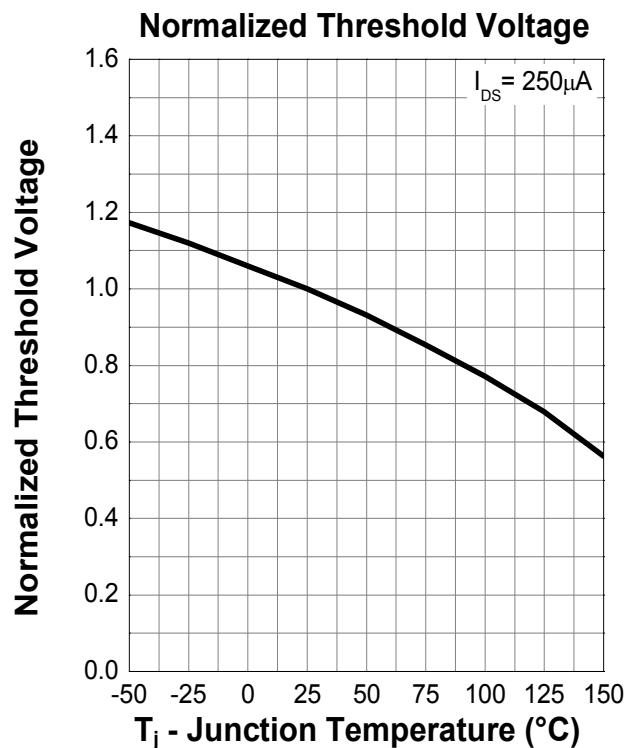
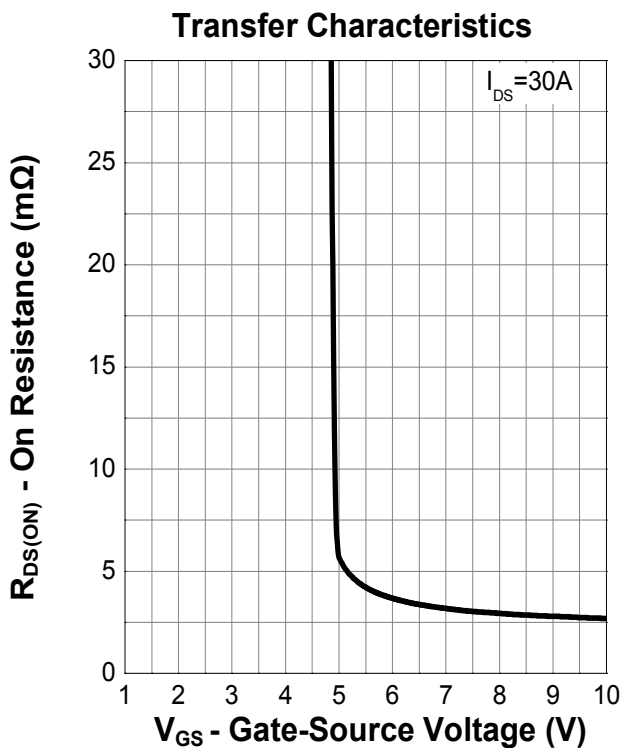
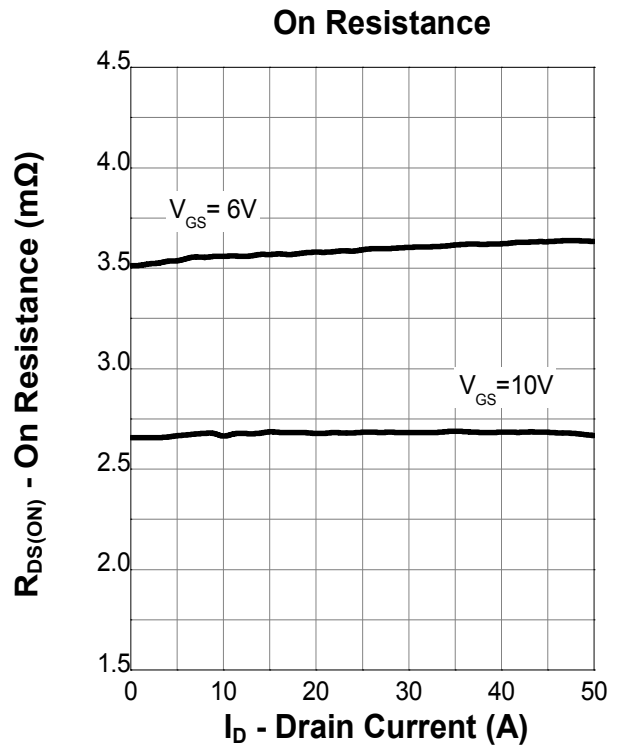
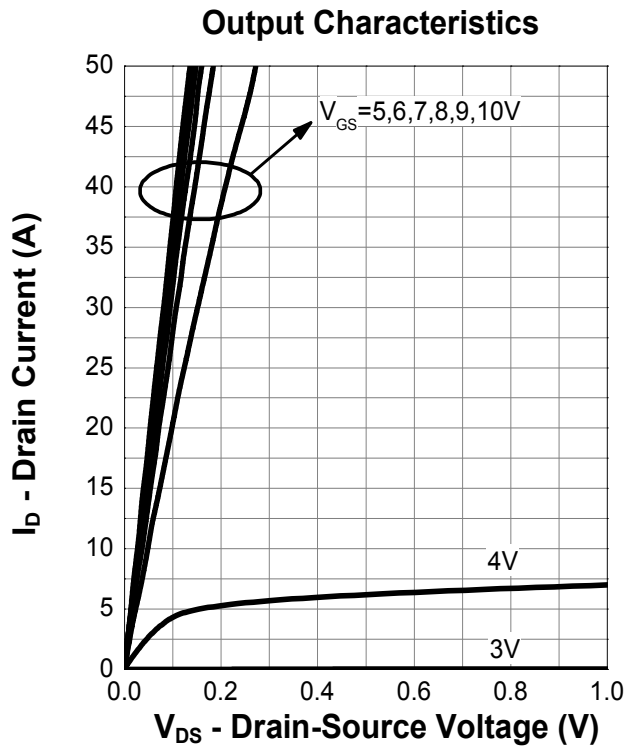
Current Capability



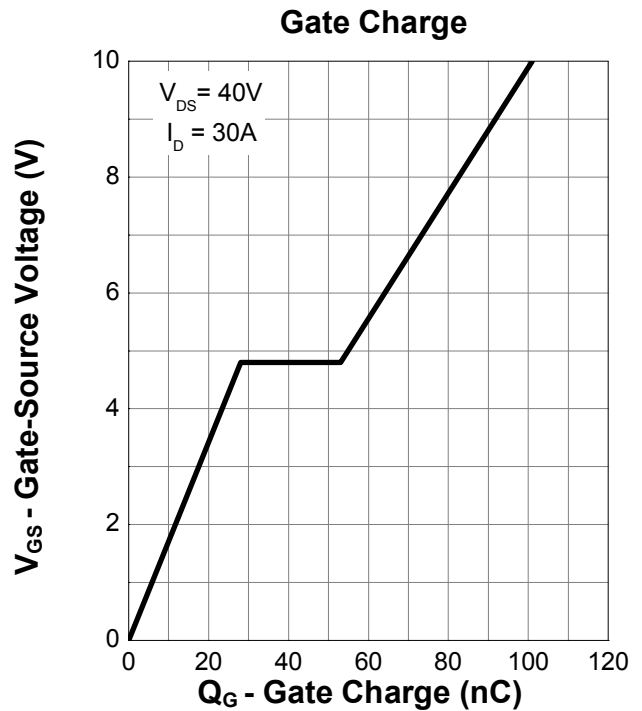
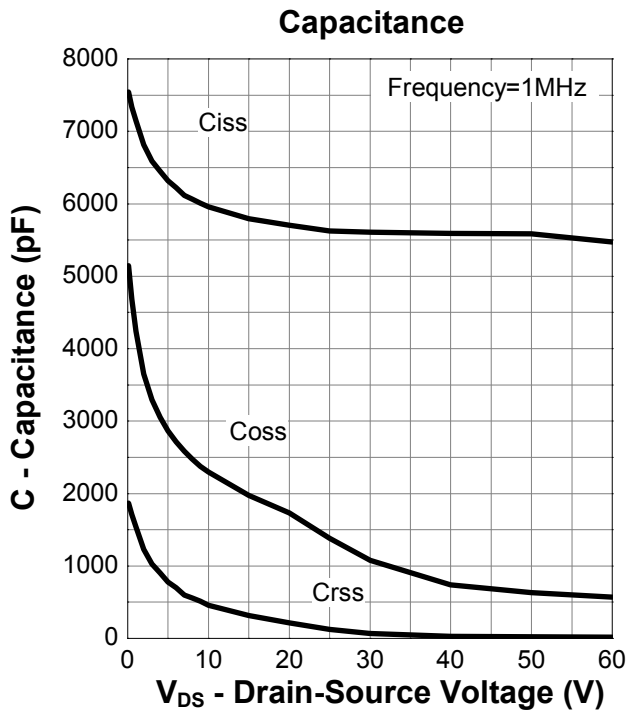
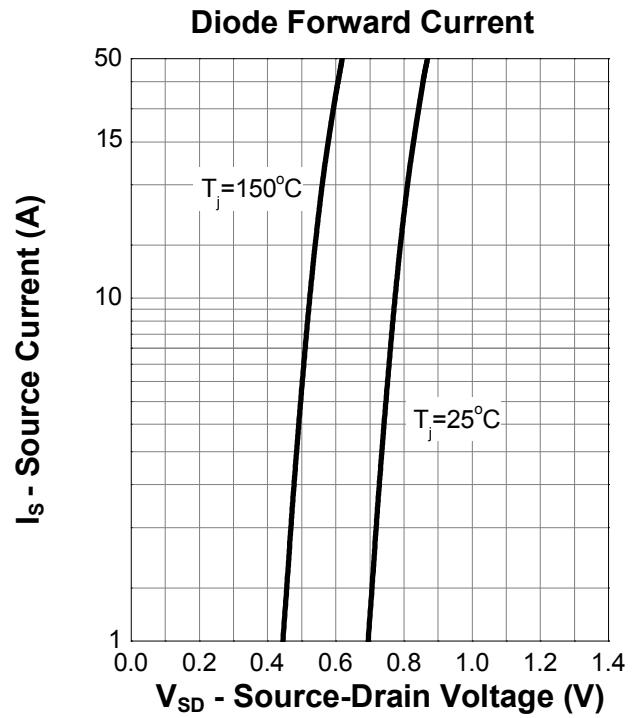
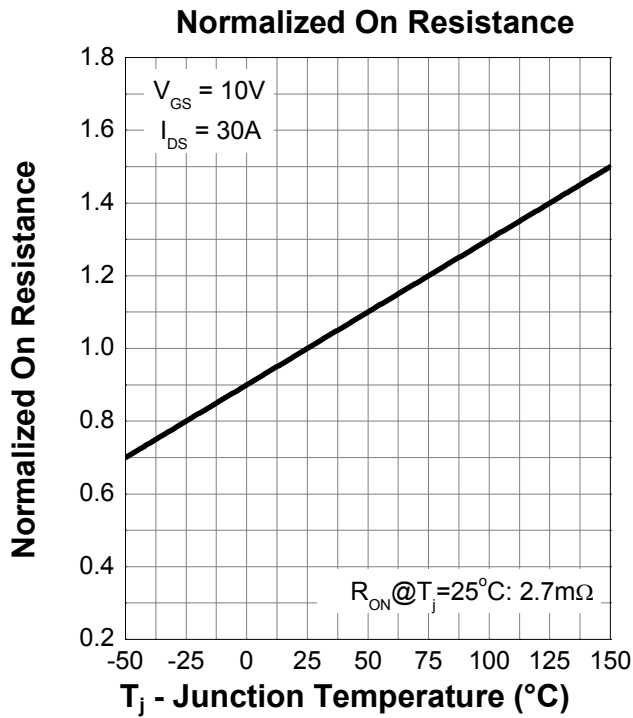
T_J - Mounting Point Temp. (°C)



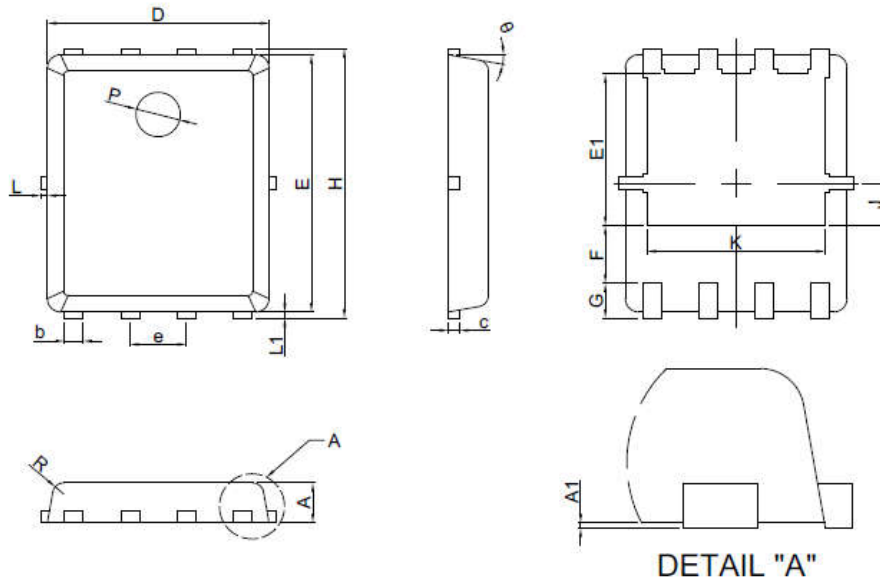
Typical Characteristics (cont.)



Typical Characteristics (cont.)



PDFN5X6 Package Dimensions



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	0.80	1.00
A1	0.00	0.05
b	0.35	0.49
c	0.254REF	
D	4.90	5.10
F	1.40REF	
E	5.70	5.90
e	1.27BSC	
H	5.95	6.20
L1	0.10	0.18
G	0.60REF	
K	4.00REF	
L	-	0.15
J	0.95BSC	
P	1.00REF	
E1	3.40REF	
θ	6°	14°
R	0.25REF	