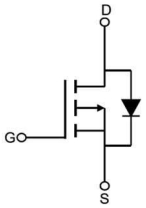
AP18P10K

P-Channel Enhancement Mosfet

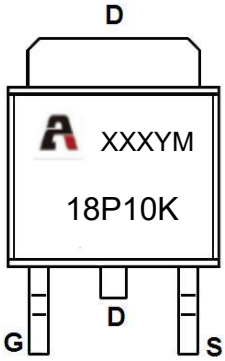
DATA SHEET

Features

- -100V,-18A
  $R_{DS(ON)}$<112mΩ @V_{GS}=-10V TYP: 86mΩ
  $R_{DS(ON)}$ <120mΩ @V_{GS}=-4.5V TYP: 90mΩ
- Low Gate Charge
- 100% UIS Tested, 100% DVDS Tested
- High Power and current handing capability
- Lead free product is acquired



Schematic Diagram



Marking and pin assignment

Applications

- Battery management
- Motor control and drive
- UPS (Uninterrupible Power Supplies)

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
18P10K	AP18P10K	TO-252	-	-	2500

ABSOLUTE MAXIMUM RATINGS (T_J=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-100	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current (Tc=25℃)	I_D	-18	A
Continuous Drain Current (Tc=100℃)	I_D	-12.5	A
Pulsed Drain Current (1)	I_{DM}	-72	A
Drain Power Dissipation	P_D	79	W
Single Pulsed Avalanche Energy(2)	E_{AS}	156	mJ
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.9	℃/W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	91	℃/W
Junction Temperature	T_J	-55~ +175	℃
Storage Temperature	T_{STG}	-55~ +175	℃

MOSFET ELECTRICAL CHARACTERISTICS(T_J=25℃ unless otherwise noted)

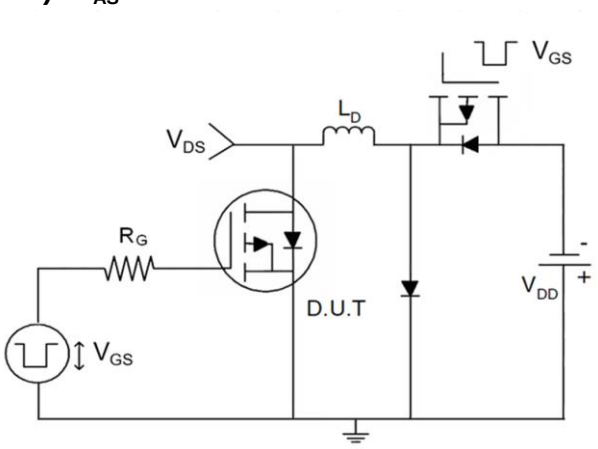
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D =-250μA	-100	-	-	V
Zero gate voltage drain current	I_{DSS}	V_{DS} =-100V, V_{GS} = 0V	-	-	-1	μA
Gate-body leakage current	I_{GSS}	V_{GS} =±20V, V_{DS} = 0V	-	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	V_{DS} =V_{GS}, I_D =-250μA	-1.0	-	-2.5	V
Drain-source on-resistance	$R_{DS(on)}$	V_{GS} =-10V, I_D =-10A	-	86	112	mΩ
Drain-source on-resistance	$R_{DS(on)}$	V_{GS} =-4.5V, I_D =-8A	-	90	120	mΩ
Dynamic characteristics						
Input Capacitance	C_{iss}	V_{DS} =-50V, V_{GS}=0V, f=1MHz	-	3769	-	pF
Output Capacitance	C_{oss}		-	72	-	
Reverse Transfer Capacitance	C_{rss}		-	66	-	
Switching characteristics						
Turn-on delay time	$t_{d(on)}$	V_{DD}=-50V, I_D=-10A, R_G=9.1Ω, V_{GS}=-10V	-	11.6	-	ns
Turn-on rise time	t_r		-	18	-	
Turn-off delay time	$t_{d(off)}$		-	115	-	
Turn-off fall time	t_f		-	42	-	
Total Gate Charge	Qg	V_{DS}=-50V, I_D=-10A, V_{GS}=-10V	-	72	-	nC
Gate-Source Charge	Qgs		-	8.4	-	
Gate-Drain Charge	Qgd		-	17.5	-	
Source-Drain Diode characteristics						
Diode Forward voltage[3]	V_{SD}	T_J=25℃, V_{GS} =0V, I_S=-10A	-	-	-1.2	V
Diode Forward current	I_S	T_C=25℃	-	-	-18	A
Body Diode Reverse Recovery Time	trr	T_J=25℃, I_F=-10A,di/dt=100A/us	-	45	-	ns
Body Diode Reverse Recovery Charge	Qrr		-	99	-	nC

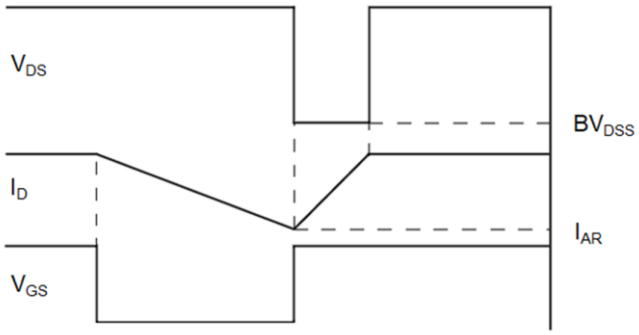
Notes:

1.Repetitive Rating: Pulse width limited by maximum junction temperature.
2.E_{AS} condition: T_J=25℃,V_{DD}=50V,V_G=-10V, Rg=25Ω, L=0.5mH.
3.Repetitive Rating: Pulse width limited by maximum junction temperature.

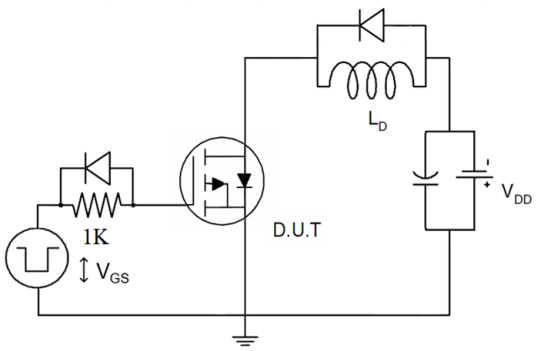
Test Circuit

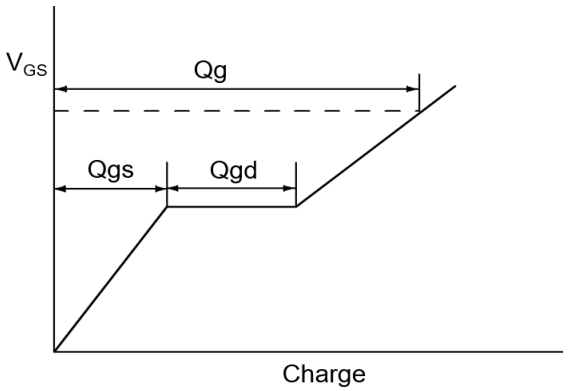
1) E_{AS} Test Circuits



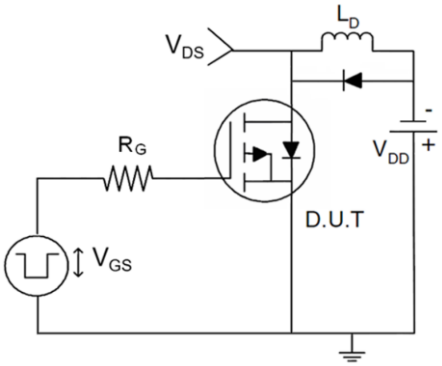


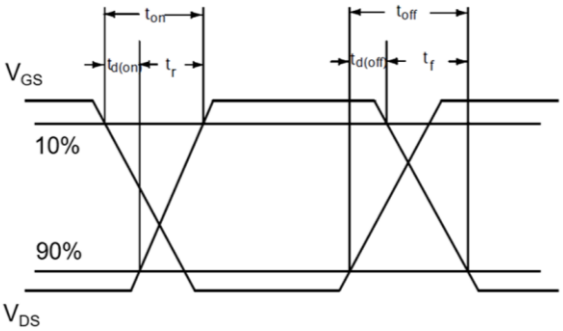
2) Gate Charge Test Circuit





3) Switch Time Test Circuit





Typical Characteristics

Figure 1. Output Characteristics

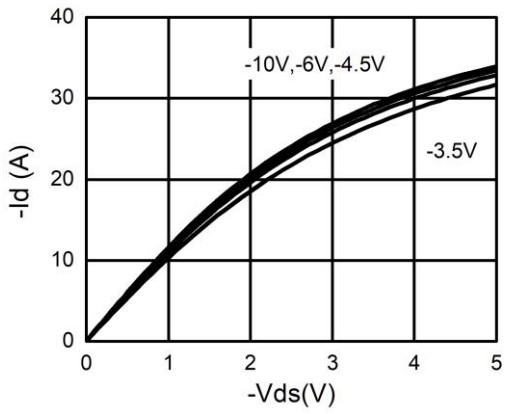


Figure 2. Transfer Characteristics

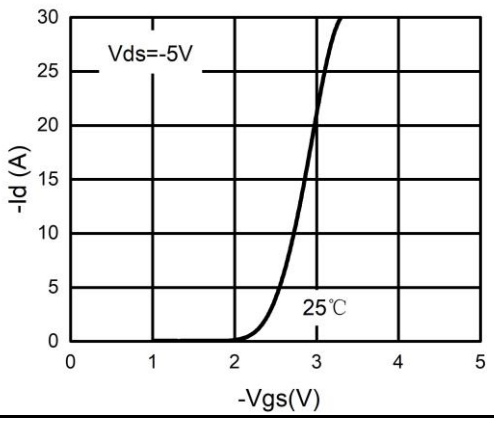


Figure 3. Power Dissipation

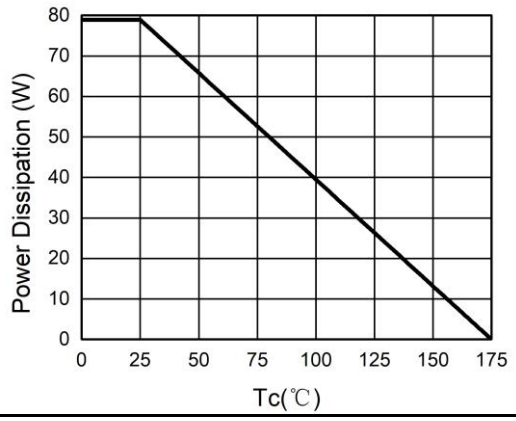


Figure 4. Drain Current

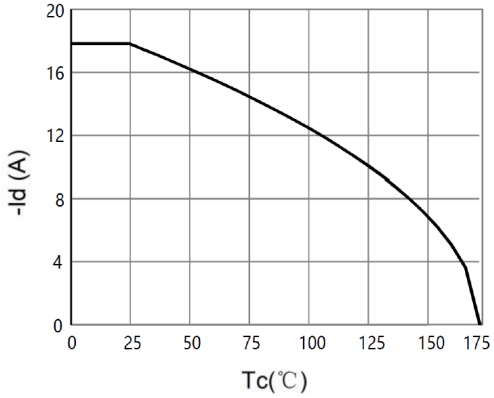


Figure 5. BV_{DSS} vs Junction Temperature

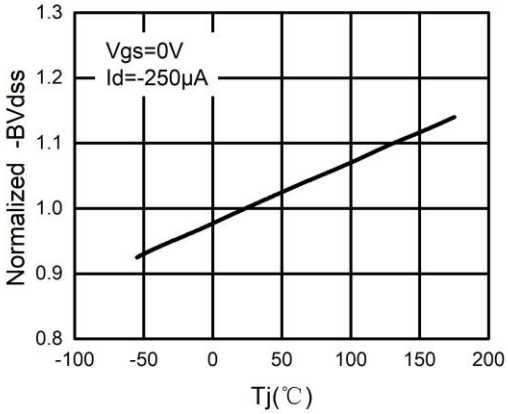
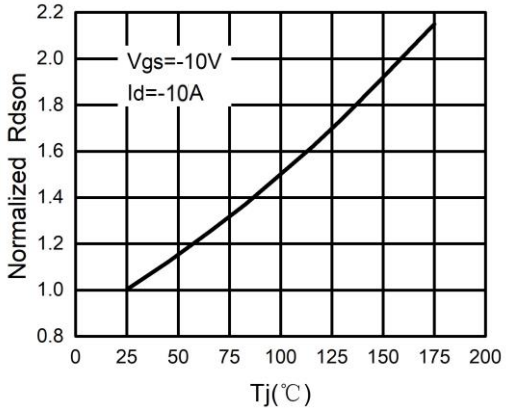


Figure 6. $R_{DS(ON)}$ vs Junction Temperature



Typical Characteristics

Figure 7. Gate Charge Waveforms

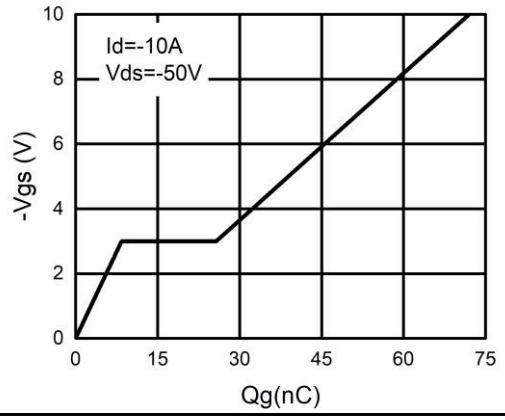


Figure 8. Capacitance

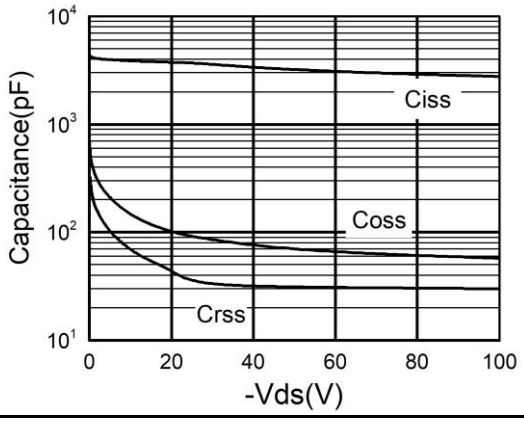


Figure 9. Body-Diode Characteristics

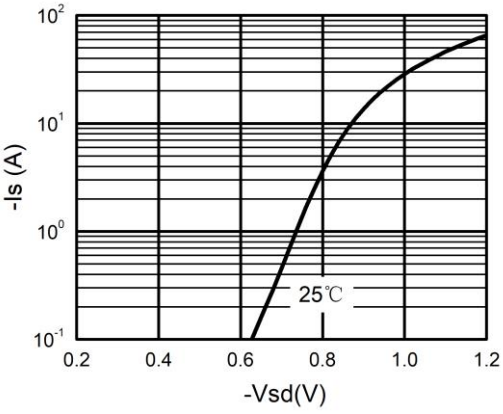
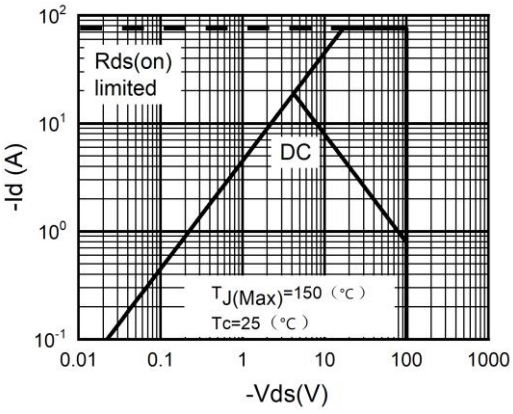
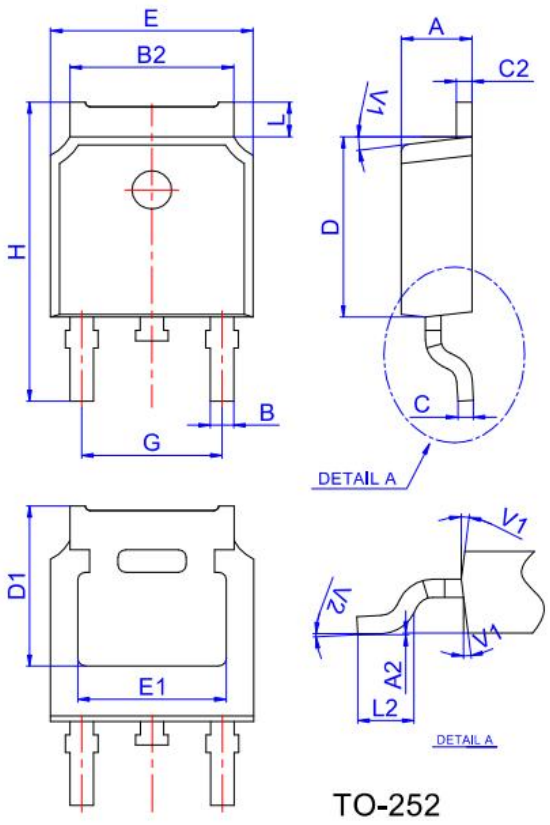


Figure 10. Maximum Safe Operating Area



TO-252 Package Information



TO-252

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

Revision History

Revision	Release	Remark
V1.0	2024/01/17	Initial Release

Disclaimer

The information given in this document describes the independent performance of the product,but similar performance is not guaranteed under other working conditions,and cannot be guaranteed when installed with other products or equipment.To achieve the required performance of the product in actual scenarios,the customer should conduct a complete application test to assess the functionality of the product.

Allpower assumes no responsibility for equipment failures result from using products at values that exceed the ratings,operating conditions,or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which
requires high reliability.Customers using or selling these products for use in medical,life-saving,or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements,the information described or contained herein may be changed without prior notice.