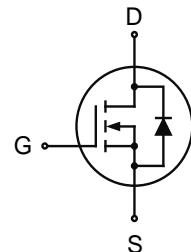
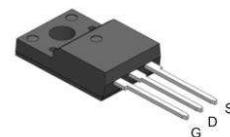


Feature

- 700V,20 A
RDS(ON) $\leq 0.55 \Omega$ @ VGS=10V, TYP=0.46 Ω
- Fast Switching
- Low ON Resistance($R_{DS(on)} \leq 0.55\Omega$)
- Low Gate Charge (Typical Data:61nC)
- 100% Single Pulse avalanche energy Test
- Halogen Free



Schematic Diagram



Application

- Switch Mode Power Supply (SMPS)
 - Uninterruptible Power Supply (UPS)
 - Power Factor Correction (PFC)
- TO-220F

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
20N70F	AP20N70F	TO-220F	-	-	1000

ABSOLUTE MAXIMUM RATINGS ($T_J=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	700	V
Gate-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current ($T_C = 25^\circ C$)	I_D	20	A
Continuous Drain Current ($T_C = 100^\circ C$)	I_D	12.6	A
Pulsed Drain Current ⁽¹⁾	I_{DM}	80	A
Power Dissipation	P_D	250	W
Single Pulse Avalanche Energy ⁽²⁾	E_{AS}	600	mJ
Junction to case ⁽⁴⁾	$R_{\theta JC}$	0.5	$^\circ C/W$
Junction to Ambient ⁽⁴⁾	$R_{\theta JA}$	62.5	$^\circ C/W$
Junction Temperature	T_J	150	$^\circ C$
Storage Temperature	T_{STG}	-55~+150	$^\circ C$

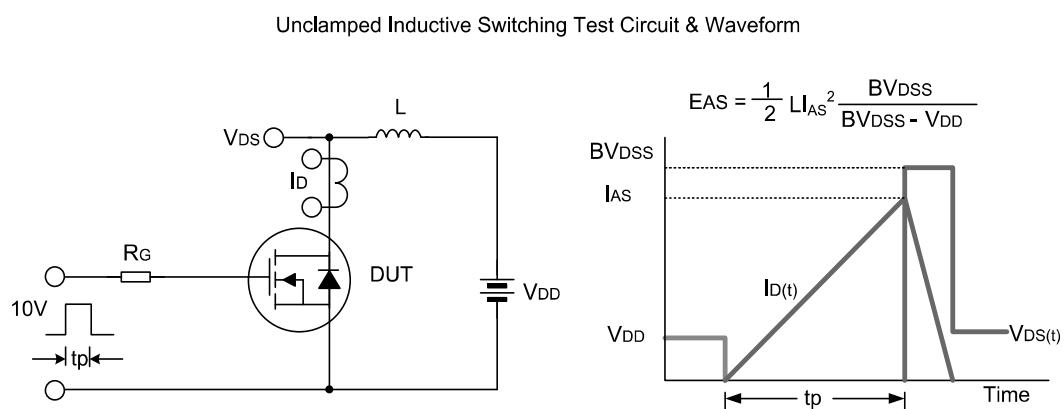
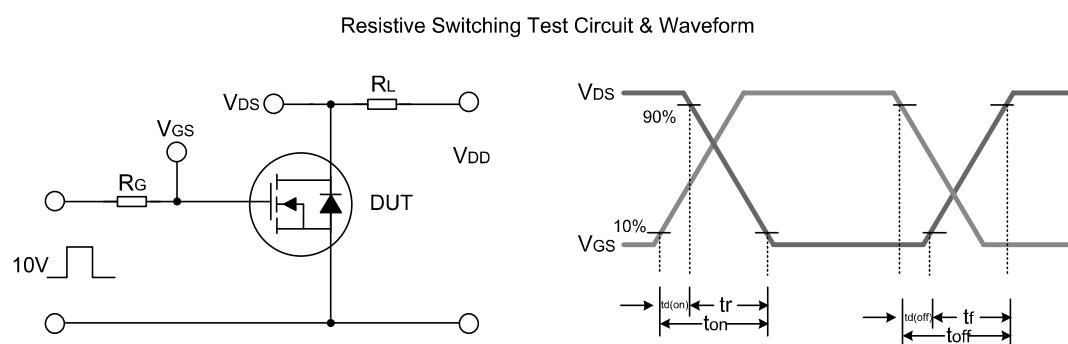
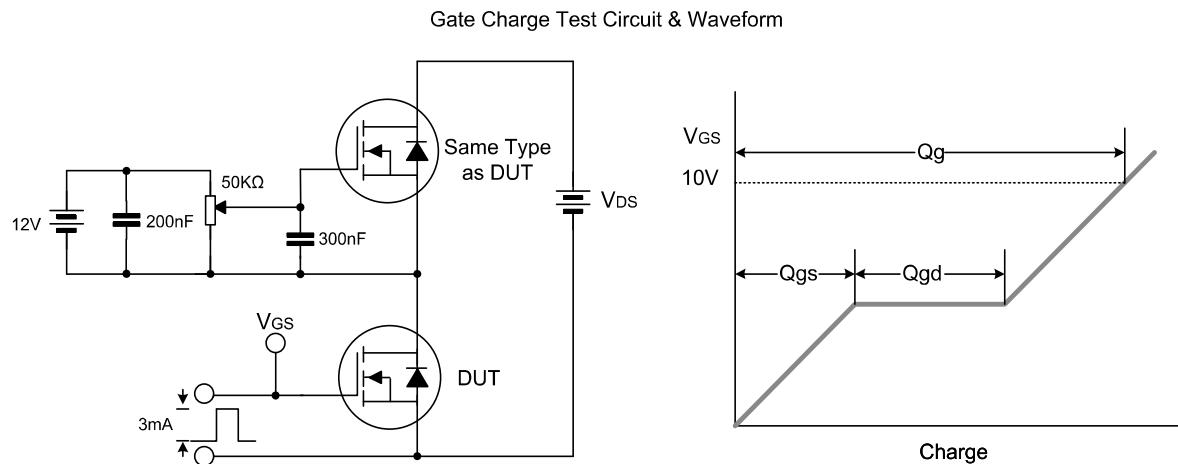
MOSFET ELECTRICAL CHARACTERISTICS($T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	700	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = 700\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$	-	-	1	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 30\text{V}, V_{\text{DS}} = 0\text{V}$	-	-	± 100	nA
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2.0	-	4	V
Drain-source on-resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 10\text{A}$	-	0.46	0.55	Ω
Dynamic characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 25\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$	-	3290	-	pF
Output Capacitance	C_{oss}		-	240	-	
Reverse Transfer Capacitance	C_{rss}		-	5	-	
Forward Transconductance	G_{fs}	$V_{\text{DS}} = 15\text{V}, I_D = 10\text{A}$		18		S
Switching characteristics						
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 350\text{V}, I_D = 20\text{A}, V_{\text{GS}} = 10\text{V}, R_G = 25\Omega$	-	34	-	ns
Turn-on rise time	t_r		-	44	-	
Turn-off delay time	$t_{\text{d}(\text{off})}$		-	75	-	
Turn-off fall time	t_f		-	53	-	
Total Gate Charge	Q_g	$V_{\text{DS}} = 560\text{V}, I_D = 20\text{A}, V_{\text{GS}} = 10\text{V}$	-	61	-	nC
Gate-Source Charge	Q_{gs}		-	15	-	
Gate-Drain Charge	Q_{gd}		-	18.5	-	
Source-Drain Diode characteristics						
Diode Forward voltage	V_{SD}	$V_{\text{GS}} = 0\text{V}, I_S = 10\text{A}$	-	-	1.5	V
Diode Forward current	I_S		-	-	20	A
Body Diode Reverse Recovery Time	t_{rr}	$V_{\text{GS}} = 0\text{V}, I_F = 20\text{A}, dI/dt = 100\text{A}/\mu\text{s}$		700		ns
Body Diode Reverse Recovery Charge	Q_{rr}			8.3		μC

Notes:

1. Repetitive Rating:pulse width limited by maximum junction temperature.
2. L=10mH,Rg=25Ω,IAS=11A , starling TJ=25°C .
3. ISD=20A,dI/dt≤100A/us,VDD≤BVDSs,starting TJ=25°C .
4. Repetitive rating; pulse width limited by maximum junction tempera

Test Circuit



Typical Performance Characteristics

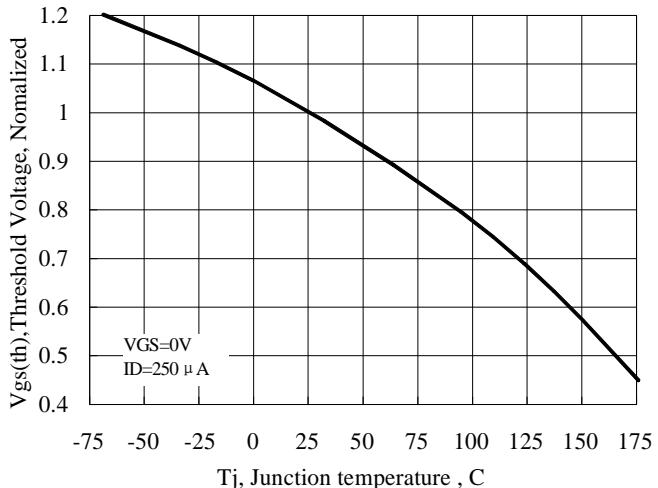


Figure 1 Typical Threshold Voltage vs Junction Temperature

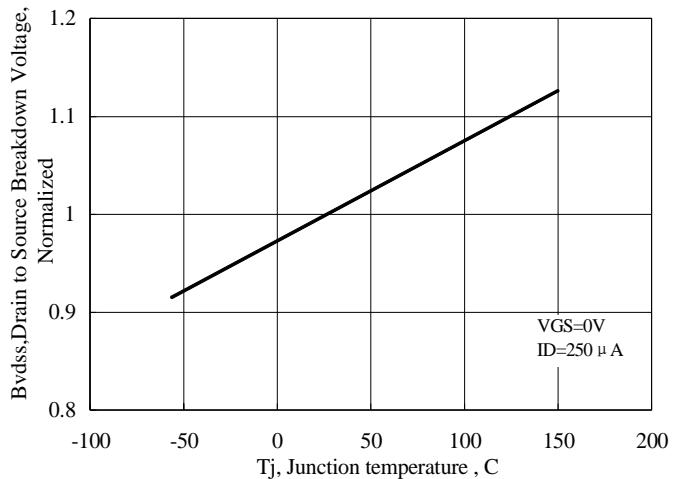


Figure 2 Typical Breakdown Voltage vs Junction Temperature

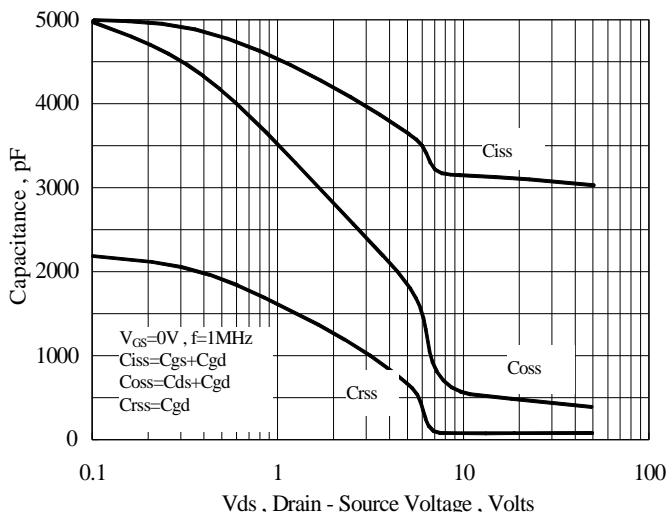


Figure 3 Typical Capacitance vs Drain to Source Voltage

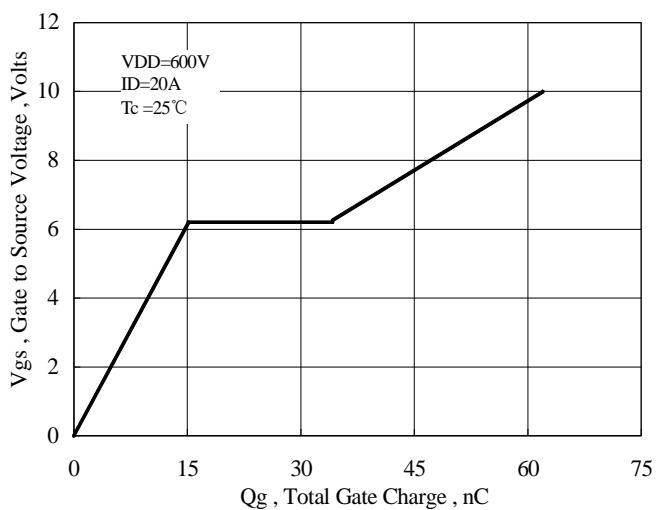


Figure 4 Typical Gate Charge vs Gate to Source Voltage

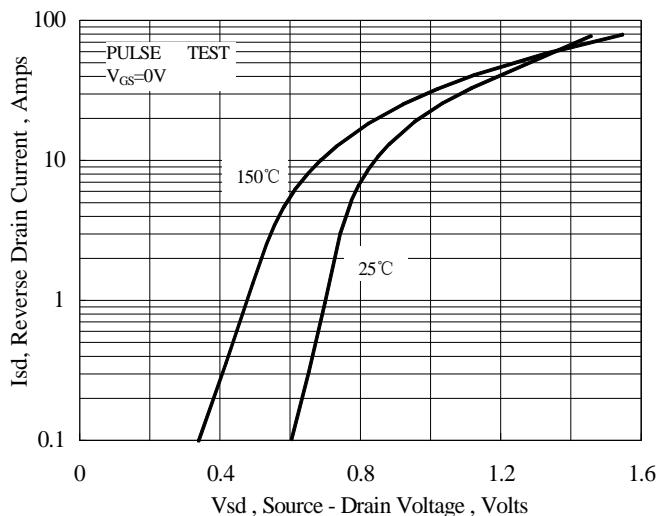


Figure 5 Typical Body Diode Transfer Characteristics

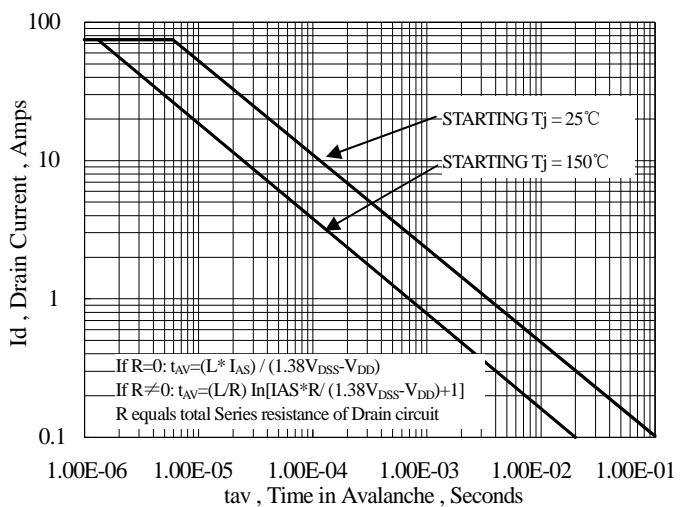


Figure 6 Unclamped Inductive Switching Capability

Typical Performance Characteristics

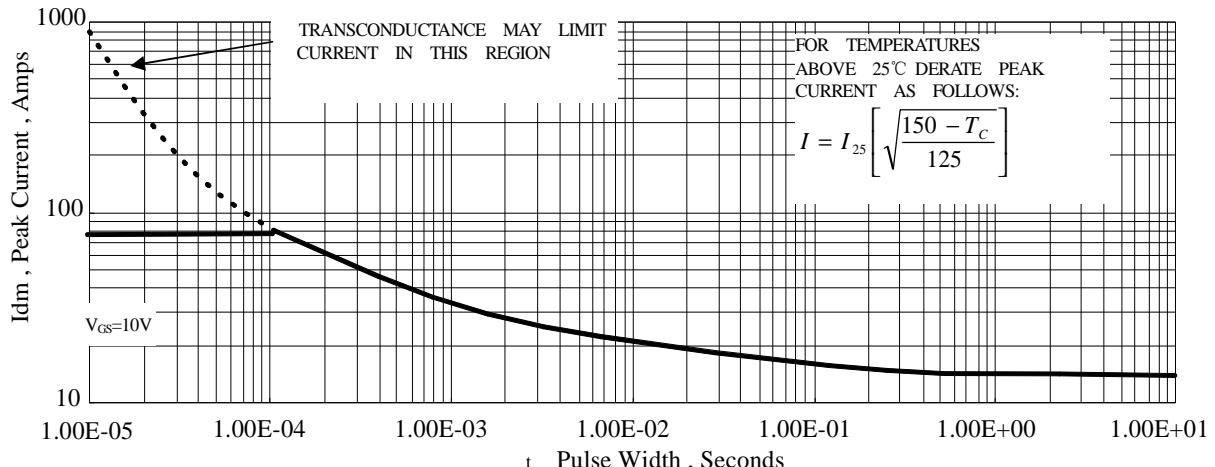


Figure 7 Maximum Peak Current Capability

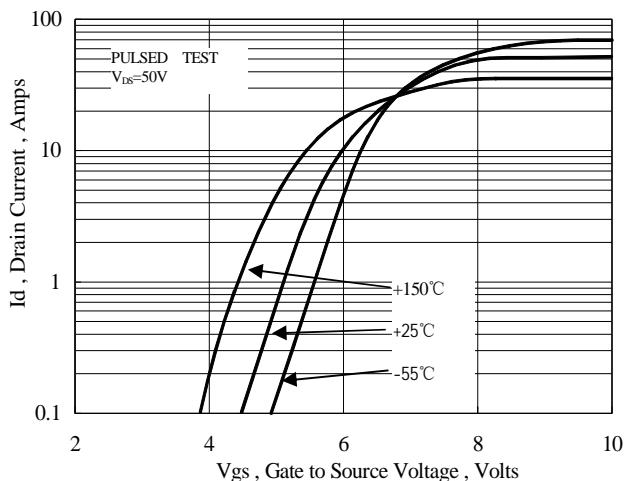


Figure 8 Typical Transfer Characteristics

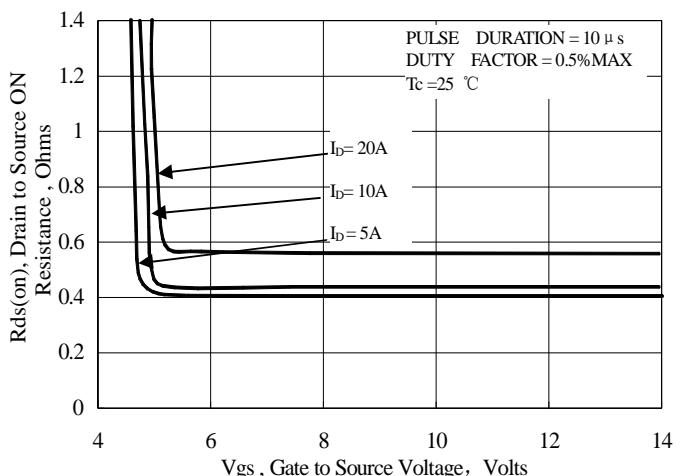


Figure 9 Typical Drain to Source ON Resistance vs Gate Voltage and Drain Current

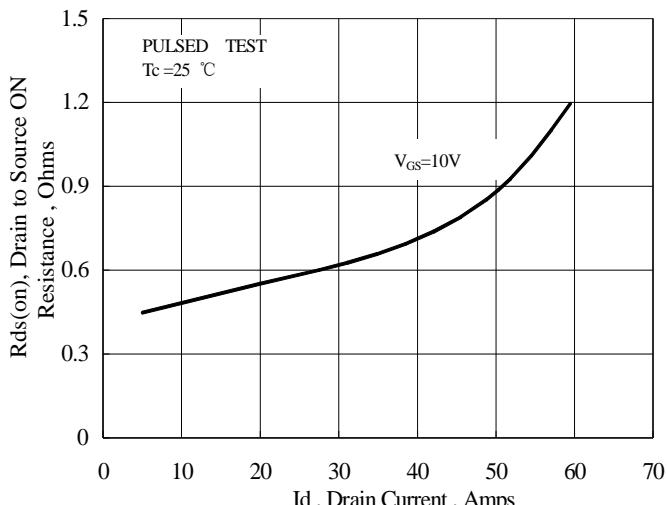


Figure 10 Typical Drain to Source ON Resistance vs Drain Current

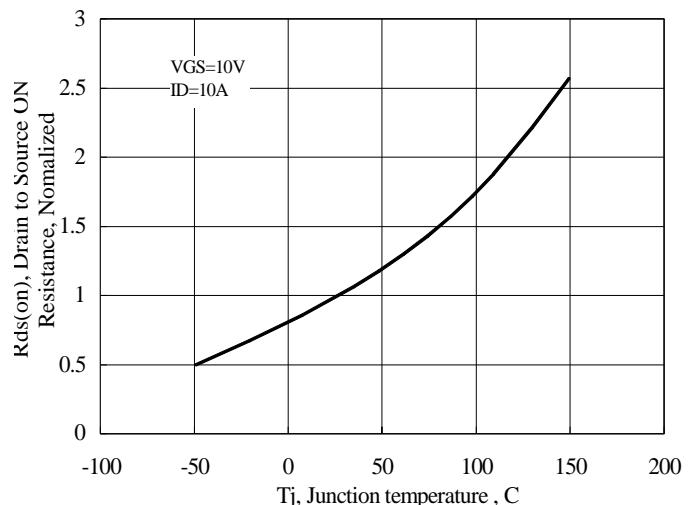


Figure 11 Typical Drain to Source ON Resistance vs Junction Temperature

Typical Performance Characteristics

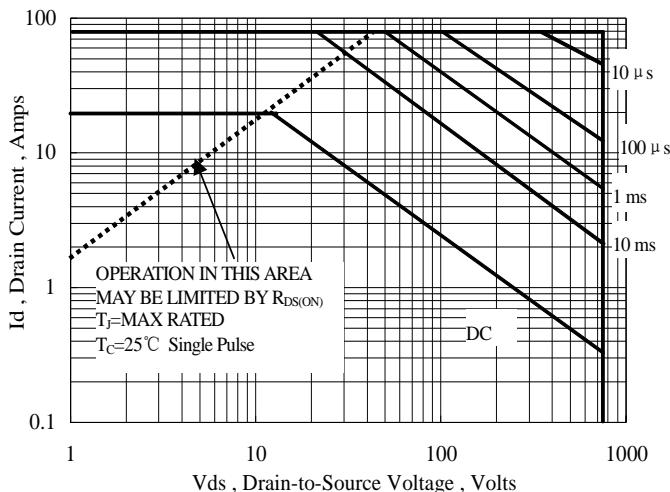


Figure 12 Maximum Forward Bias Safe Operating Area

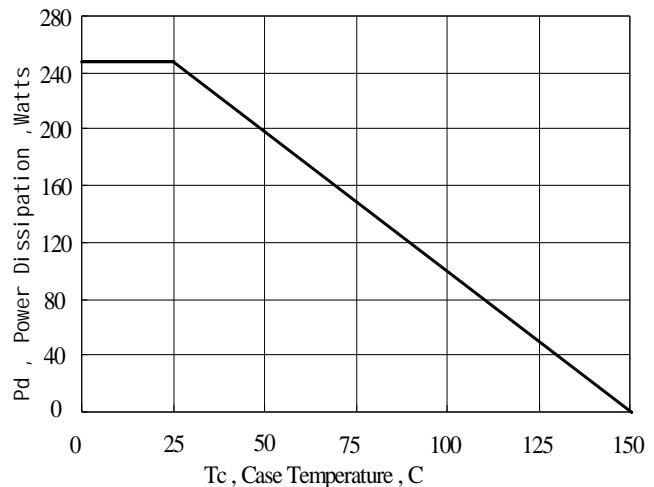


Figure 13 Maximum Power Dissipation vs Case Temperature

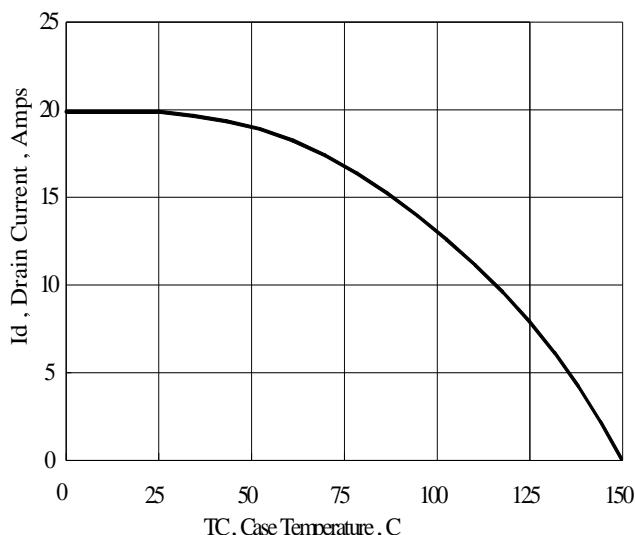


Figure 14 Maximum Continuous Drain Current vs Case Temperature

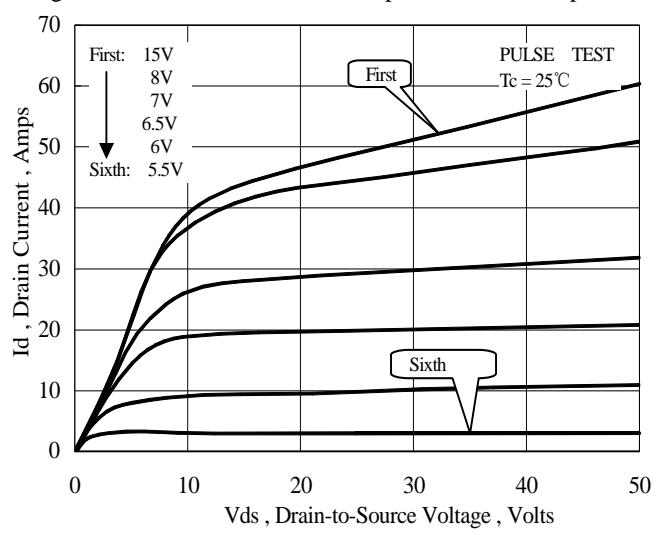


Figure 15 Typical Output Characteristics

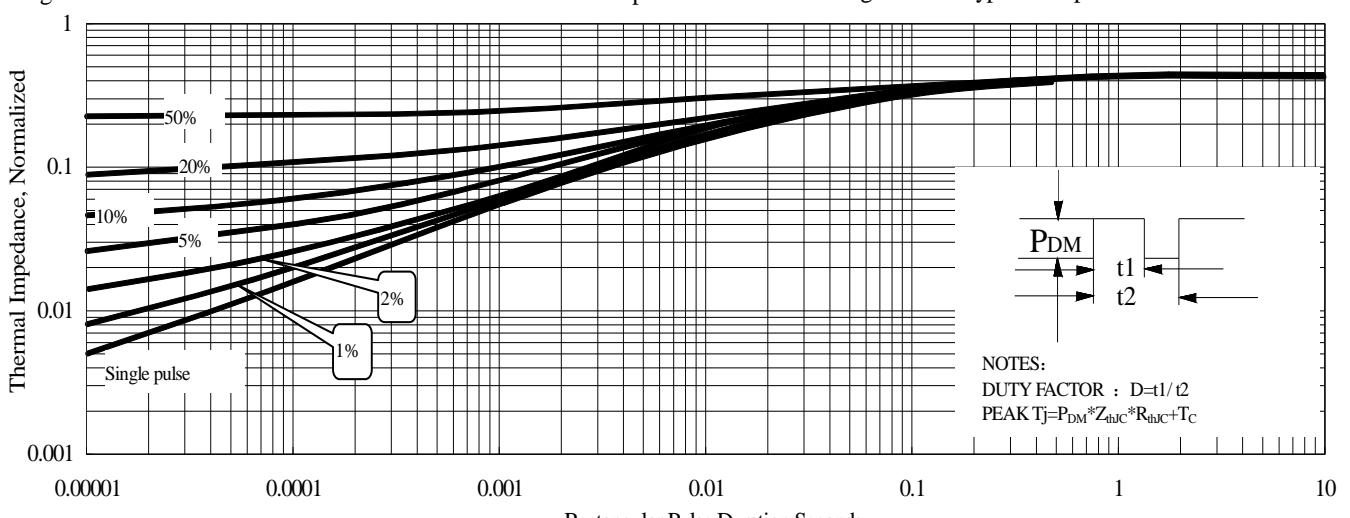
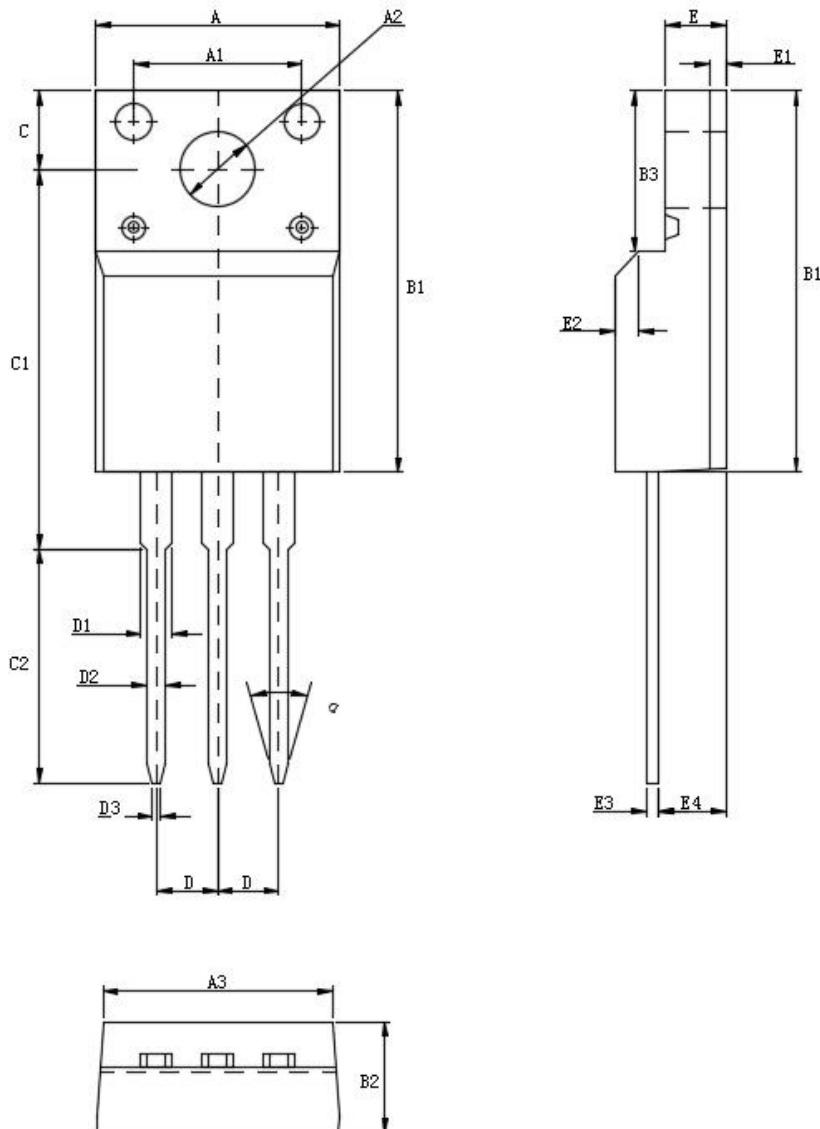


Figure 16 Maximum Effective Thermal Impedance , Junction to Case

Package Dimensions of TO-220F



UNIT: mm

SYMBOL	min	nom	max	SYMBOL	min	nom	max
A	9.80		10.60	D		2.54	
A1		7.00		D1	1.15		1.55
A2	2.90		3.40	D2	0.60		1.00
A3	9.10		9.90	D3	0.20		0.50
B1	15.40		16.40	E	2.24		2.84
B2	4.35		4.95	E1		0.70	
B3	6.00		7.40	E2		1.0×45°	
C	3.00		3.70	E3	0.35		0.65
C1	15.00		17.00	E4	2.30		3.30
C2	8.80		10.80	α (度)		30°	

Revision History

Revision	Release	Remark
V1.0	2023/06/02	Initial Release

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Allpower assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.