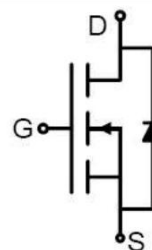


## Features

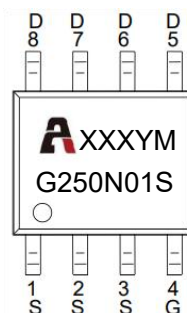
- 100V,8A  
 $R_{DS(on)} < 25m\Omega @ V_{GS}=10V$  TYP: 18m $\Omega$   
 $R_{DS(on)} < 32m\Omega @ V_{GS}=4.5V$  TYP: 25m $\Omega$
- Split Gate Trench Technology
- Lead free product is acquired
- Excellent  $R_{DS(on)}$  and Low Gate Charge

## Applications

- PWM applications
- Load Switch
- Power management



**Schematic Diagram**



**Marking and pin Assignment**

## Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
G250N01S	APG250N01S	SOP-8	-	-	4000

## ABSOLUTE MAXIMUM RATINGS ( $T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current ( $T_C=25^\circ\text{C}$ ) <sup>(1)</sup>	$I_D$	8	A
Continuous Drain Current ( $T_C=100^\circ\text{C}$ )	$I_D$	5	A
Pulsed Drain Current <sup>(1)</sup>	$I_{DM}$	32	A
Drain Power Dissipation	$P_D$	50	W
Single Pulsed Avalanche Energy <sup>(2)</sup>	$E_{AS}$	11	mJ
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	42	$^\circ\text{C/W}$
Thermal Resistance from Junction to Case	$R_{\theta JC}$	2.5	$^\circ\text{C/W}$
Junction Temperature	$T_J$	-55~ +150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55~ +150	$^\circ\text{C}$

### Notes:

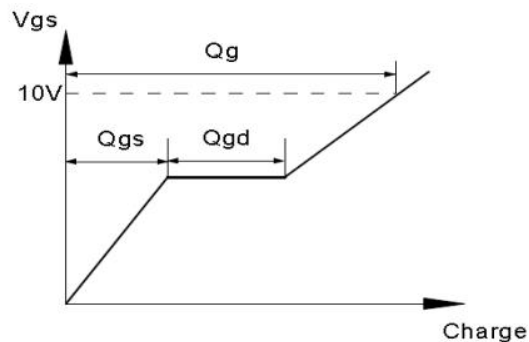
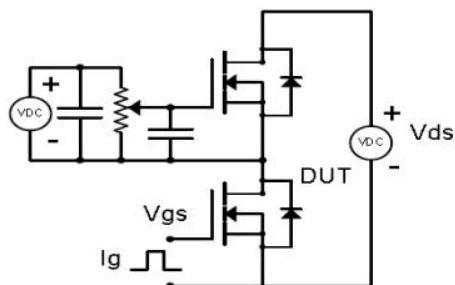
- 1) Repetitive Rating: pulse width limited by maximum junction temperature
- 2) EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{DD}=20V$ ,  $V_G=10V$ ,  $L=0.5mH$ ,  $R_g=25\Omega$ ,  $I_{AS}=6.6A$

**MOSFET ELECTRICAL CHARACTERISTICS( $T_J=25^{\circ}\text{C}$  unless otherwise noted)**

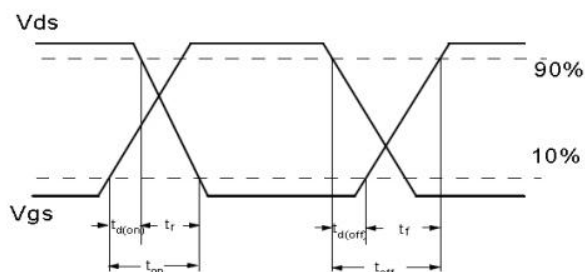
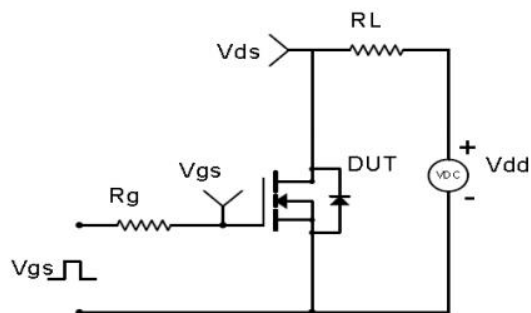
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> =250μA	100	-	-	V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> = 0V	-	-	1	μA
Gate-body leakage current	I <sub>GSS</sub>	V <sub>GS</sub> =± 20V, V <sub>DS</sub> = 0V	-	-	±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.2	1.8	2.5	V
Drain-source on-resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =6A	-	18	25	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	-	25	32	
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f = 1MHz	-	1.6	-	Ω
Dynamic characteristics						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V, VGS=0V, f=1MHz	-	822	-	pF
Output Capacitance	C <sub>oss</sub>		-	310	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	23.5	-	
Switching characteristics						
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DD</sub> =50V, I <sub>D</sub> =6A, V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω	-	15	-	ns
Turn-on rise time	t <sub>r</sub>		-	3.2	-	
Turn-off delay time	t <sub>d(off)</sub>		-	30	-	
Turn-off fall time	t <sub>f</sub>		-	7.6	-	
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =50V, I <sub>D</sub> =6A, V <sub>GS</sub> =10V	-	22.7	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	6.2	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	5.3	-	
Source-Drain Diode characteristics						
Diode Forward voltage	V <sub>SD</sub>	T <sub>J</sub> =25℃, V <sub>GS</sub> =0V, I <sub>S</sub> =6A	-	0.8	1.2	V
Diode Forward current	I <sub>S</sub>	T <sub>C</sub> =25℃	-	-	8	A
Body Diode Reverse Recovery Time	trr	T <sub>J</sub> =25℃, I <sub>F</sub> =6A,di/dt=100A/us	-	45	-	ns
Body Diode Reverse Recovery Charge	Qrr		-	59	-	nC

## Test Circuit & Waveform

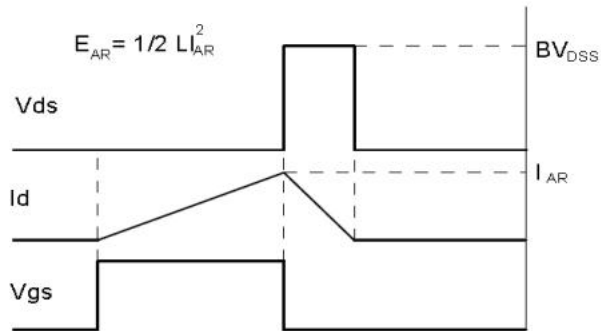
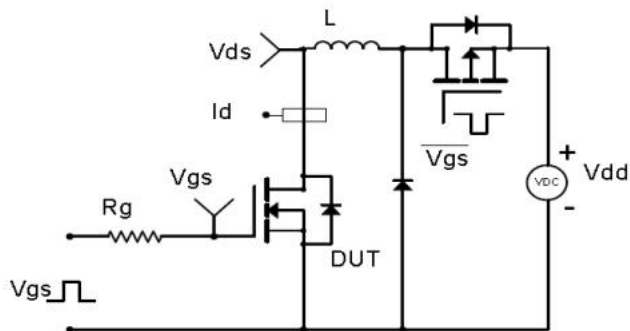
Gate Charge Test Circuit & Waveform



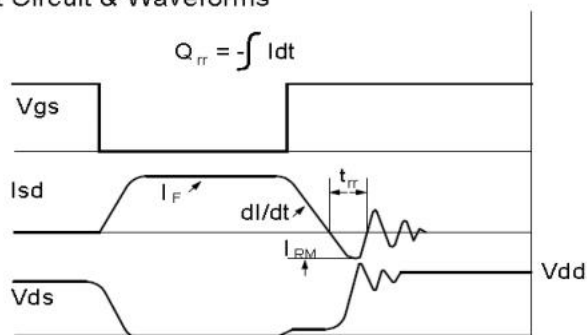
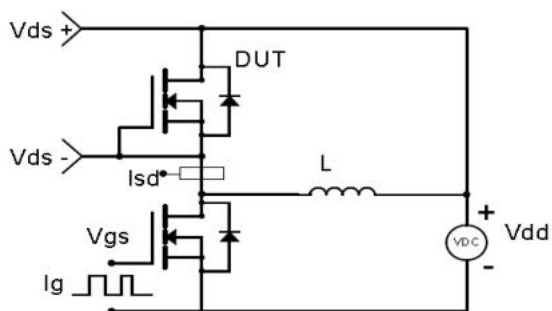
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



## Typical Characteristics

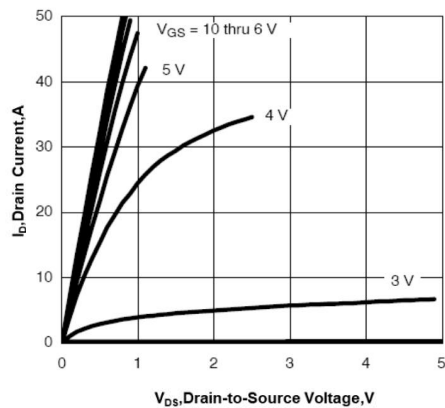


Figure 1. Output Characteristics

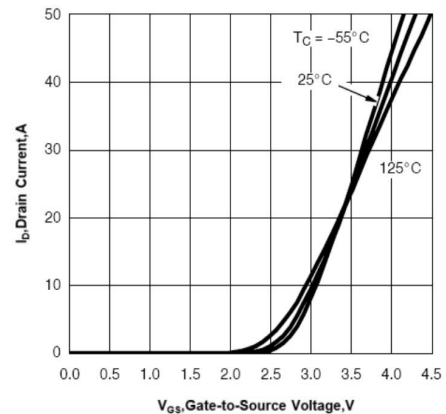


Figure 2. Transfer Characteristics

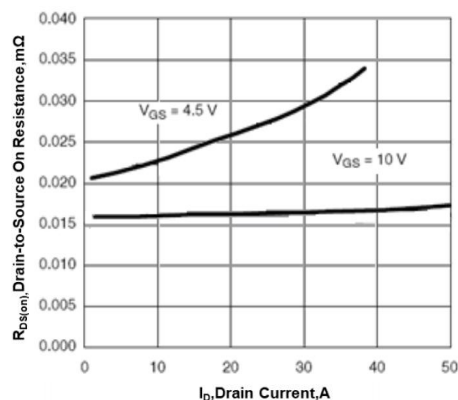


Figure 3. Drain-to-Source On Resistance  
vs Drain Current

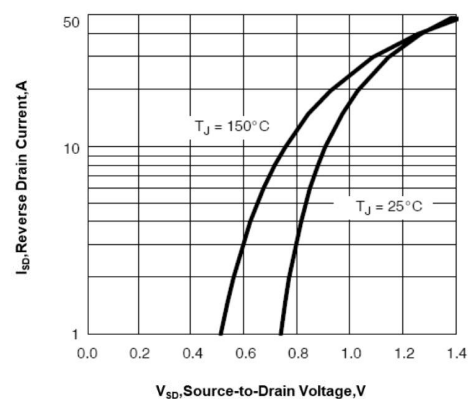


Figure 4. Body Diode Forward Voltage  
vs Source Current and Temperature

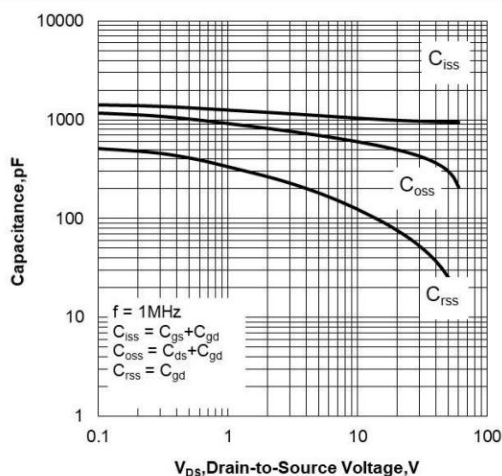


Figure 5. Capacitance Characteristics

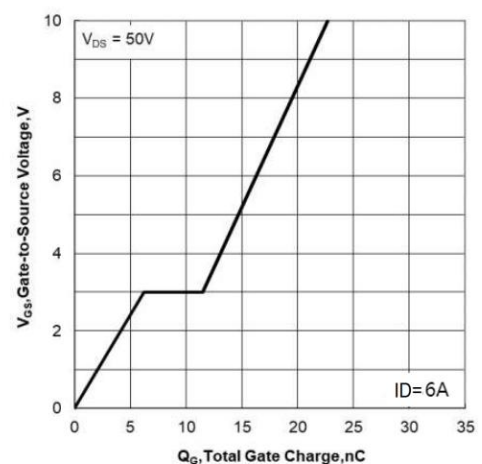


Figure 6. Gate Charge Characteristics

## Typical Characteristics

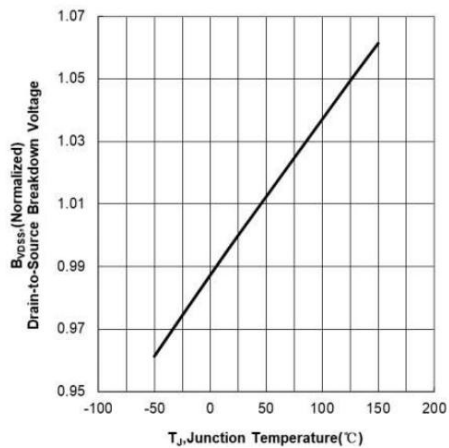


Figure 7. Normalized Breakdown Voltage vs Junction Temperature

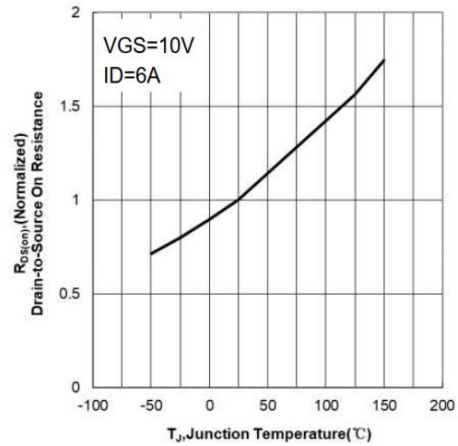


Figure 8. Normalized On Resistance vs Junction Temperature

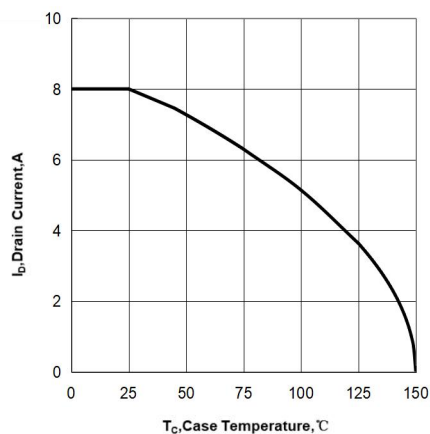


Figure 9. Maximum Continuous Drain Current vs Case Temperature

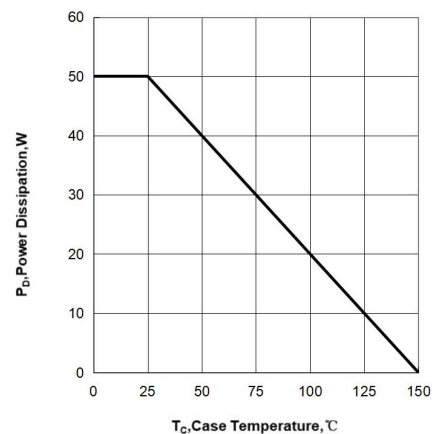


Figure 10. Maximum Power Dissipation vs Case Temperature

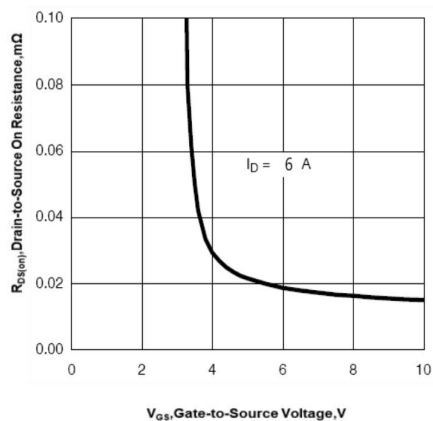


Figure 11. Drain-to-Source On Resistance vs Gate Voltage and Drain Current

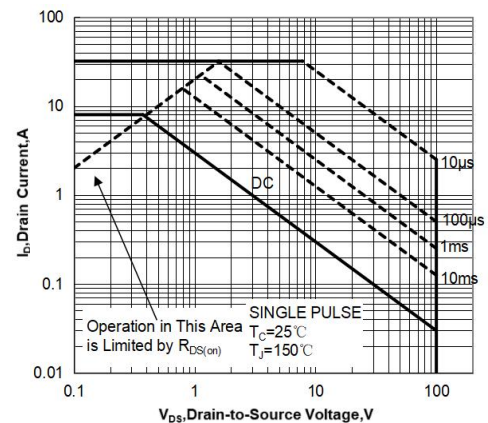


Figure 12. Maximum Safe Operating Area

Typical Characteristics

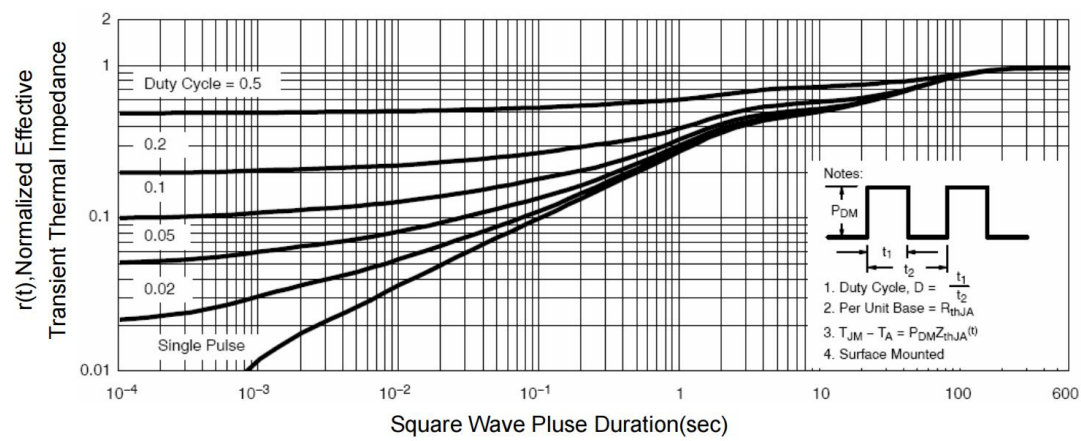
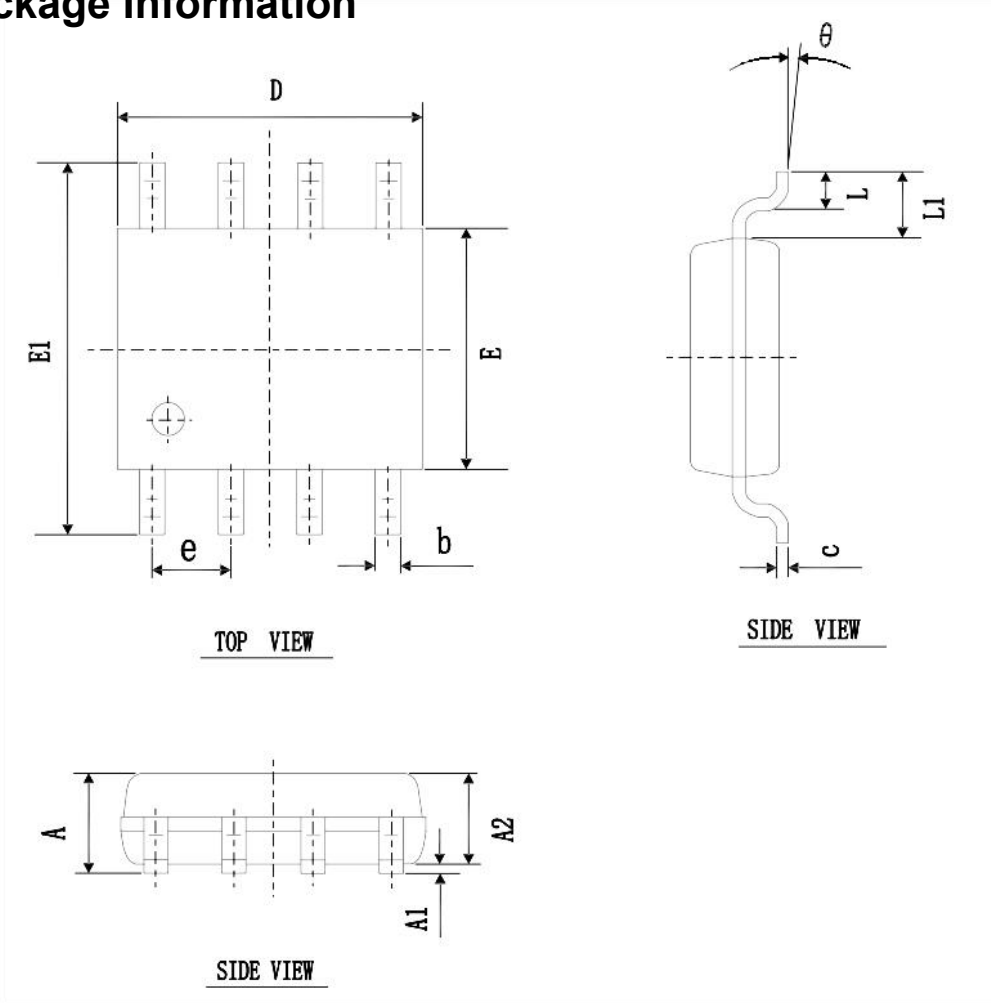


Figure 13 Normalized Maximum Transient Thermal Impedance

SOP-8 Package Information



COMMON DIMENSIONS  
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	1.35	1.60	1.80
A1	0.05	0.15	0.25
A2	1.30	1.45	1.60
b	0.30	0.40	0.50
c	0.153	0.203	0.253
D	4.80	4.90	5.00
E	3.80	3.90	4.00
E1	5.80	6.00	6.20
L	0.40	0.70	1.25
$\theta$	0°		7°
L1	1.04 REF		
e	1.27 BSC		

## Revision History

Revision	Release	Remark
V1.1	2025/04/07	Initial Release

## Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Allpower assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.